

MiniPack: Low Power 100G Fabric Architecture - Mechanical & Thermal Cooling Design

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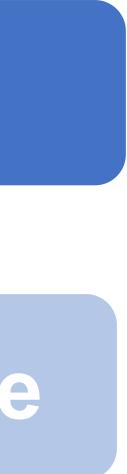
Agenda





Minipack HW Overview

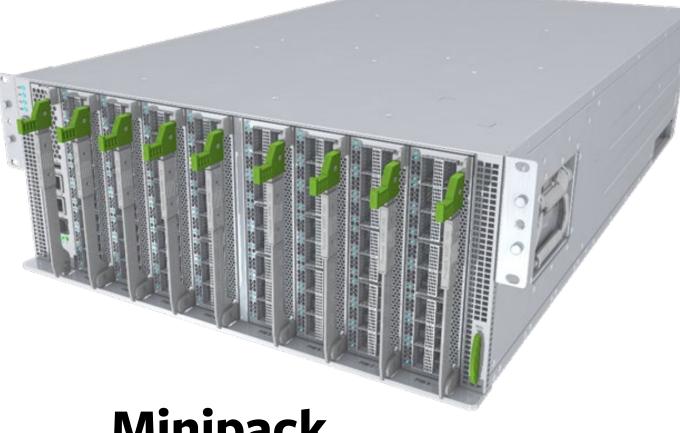
Minipack Mechanical Deep Dive





Minipack – Next-Generation 128x 100G Switch

- Adopt cutting edge switch ASIC
- **Support mature optics 100G CWDM4**
- Lower Power / Smaller Size \rightarrow



Minipack







pack

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1/2 height, ~1/2 power compared to existing Tomahawk based design – Backpack

Switch ASIC: Broadcom Tomahawk-3

Size: 4RU (vs. 8RU Backpack)

Power: ~1.4KW budgetary (At full line rate, fully populated with 128x QSFP28 CWDM4 optics) **Radix:** 128x









MiniPack

Announced during Global Summit in March 2019 Design formally accepted by OCP in June 2019. .













Minipack System Components

- Switch Main Board (SMB)
- System Control Module (SCM) FRU
 - Port Interface Module (PIM) FRU
 - PIM-16Q: Port Interface Module with 16 x QSFP28 100G



Fan Control Module (FCM): Top and Bottom



- Power Distribution System
- Horizontal Bus Bar (HBAR)
- Power Distribution Board (PDB): Left and Right



DOM (Digital Optics Monitoring) FPGA





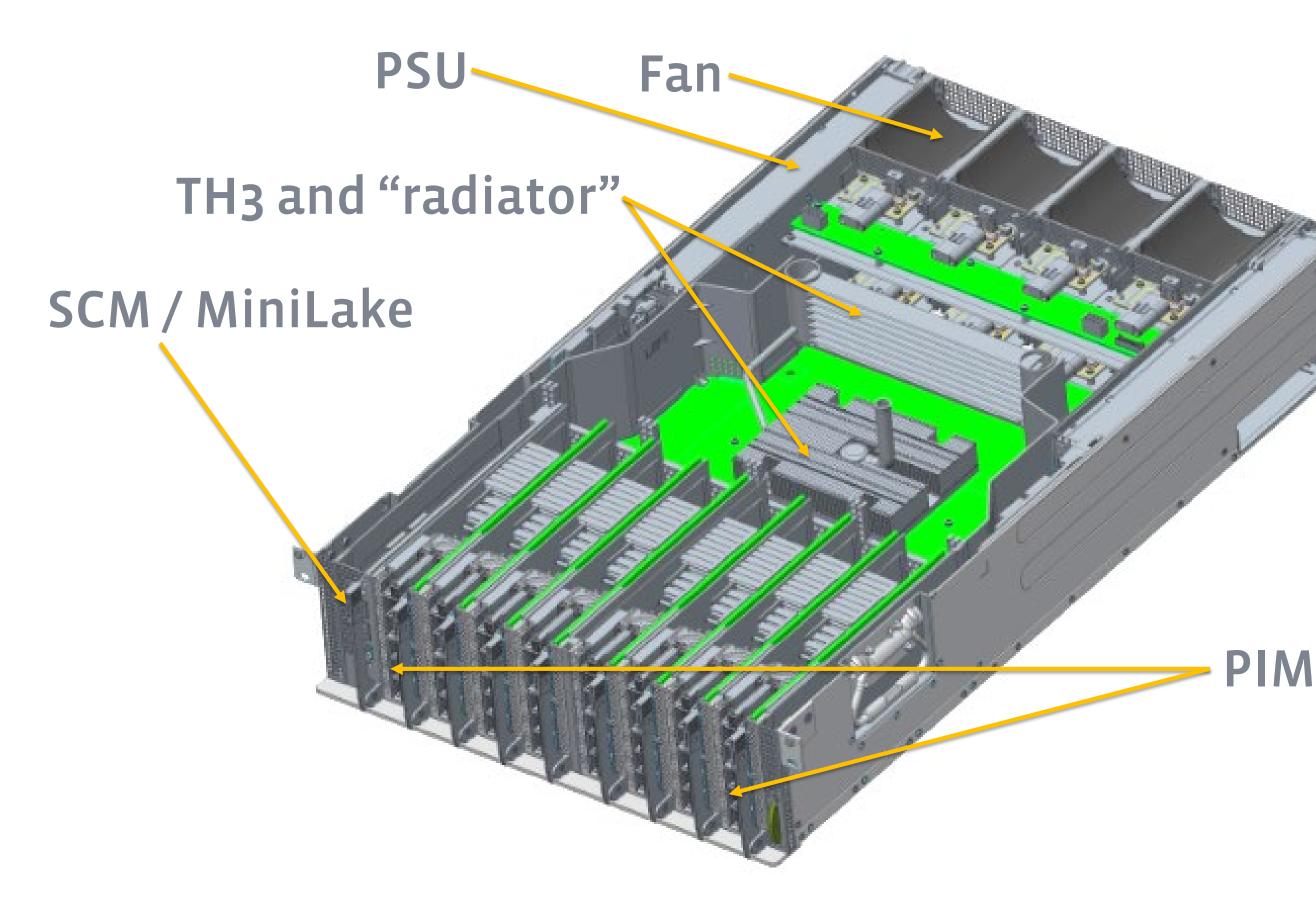
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Minipack Chassis Architecture Orthogonal-direct architecture







- Opens up airflow path for better thermal efficiency
- Supports 100G CWDM4-Lite optics with 55 °C case temperature limit
- Shortens PCB traces for lower loss
- FRU-able, modular PIM (line card)
 - Vertically oriented
 - PIM-16Q
 - Easy to explore other PIM options
- FRU-able SCM (micro-server carrier)









Where to use Minipack

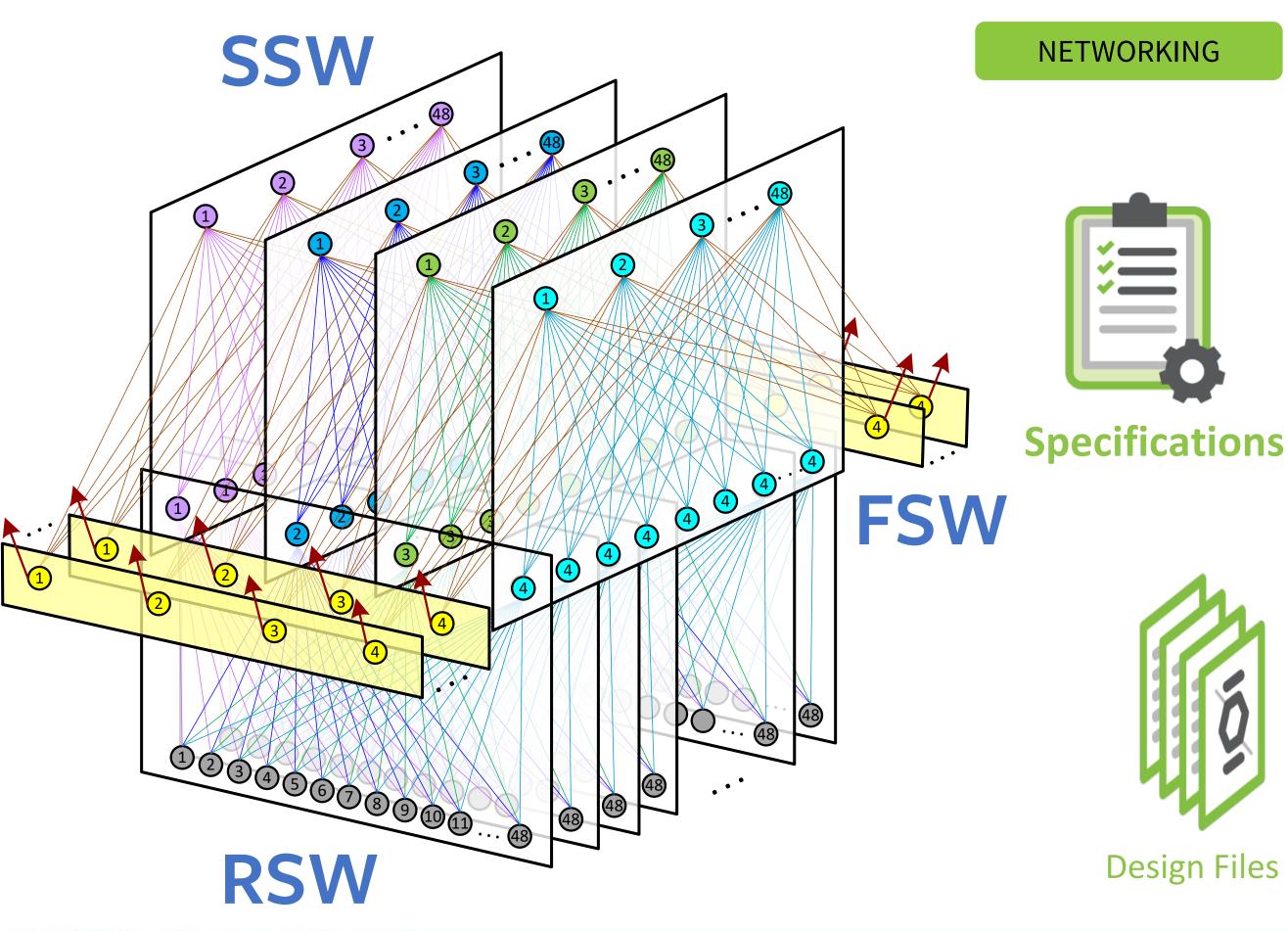


- FSW Fabric Switch
- SSW Spine Switch
- FA Fabric Aggregator



Supports multiple generations of rack switches

















Minipack: Overall Mechanical Spec.

- Chassis Dimensions: 176.2mm(H) x 440.4mm(W) x 738.1mm(D)
- Weight: Fully loaded chassis with all FRUs is 54kg
 - Chassis assy with thermal syphon
 - 8 Fantray assy
 - 8 PIM-16Q
 - 1 SCM
 - 4 -1500w PSU
- Handle: One handle and a handle pocket on each side of the chassis



















Minipack HW Overview

Minipack Mechanical Deep Dive

Specifications







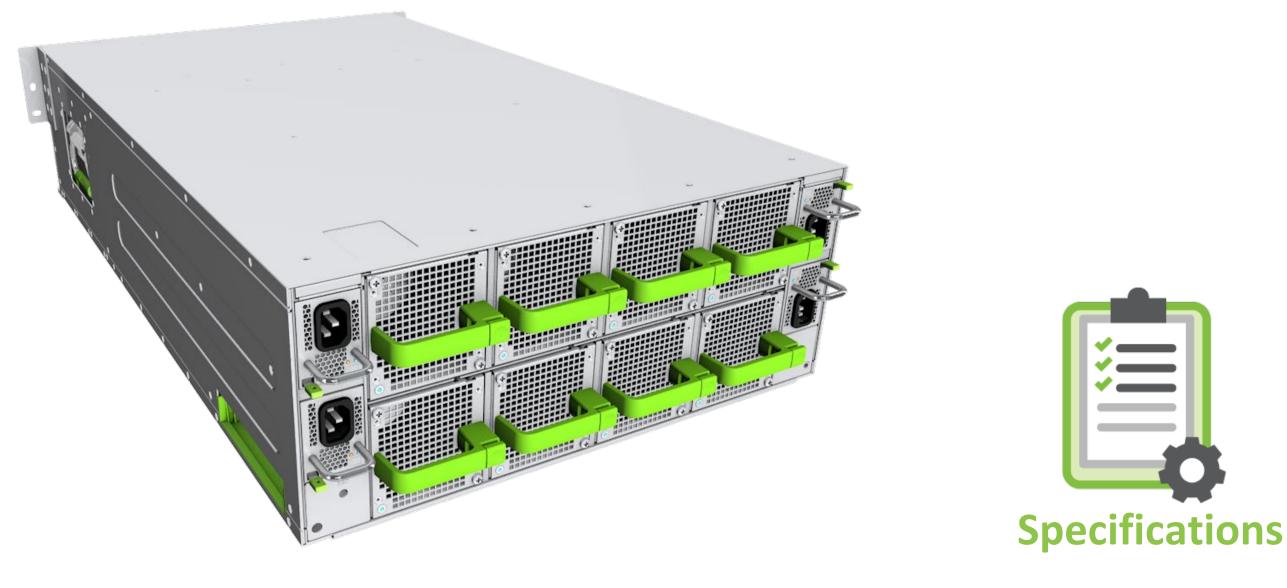
- Orthogonal direct architecture
- Disaggregated data, control, and management plane design







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Top Cross-Sectional View

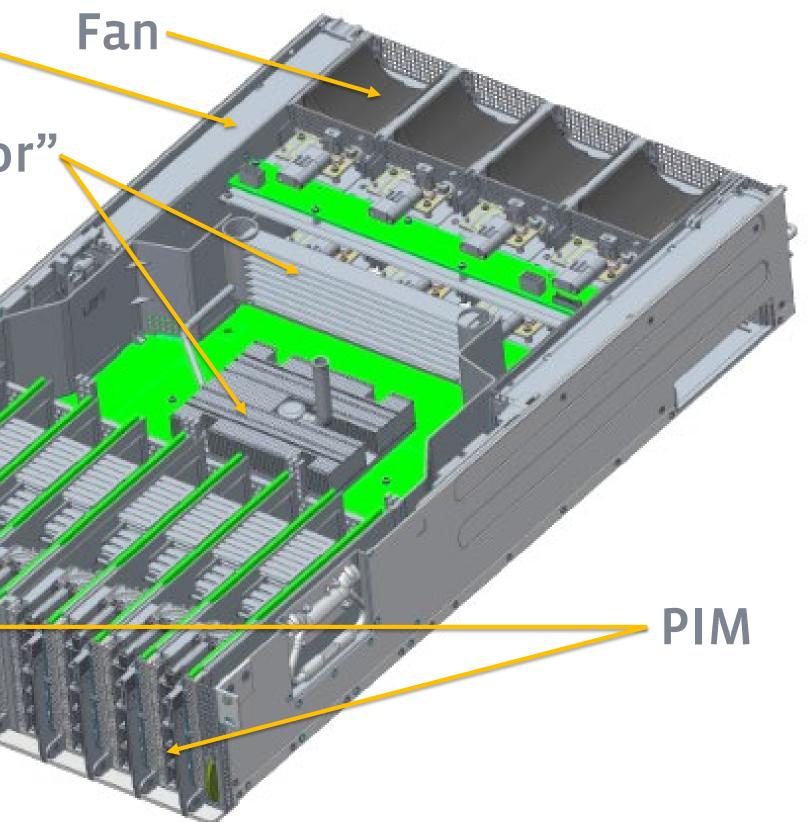


TH3 and "radiator"

SCM / MiniLake









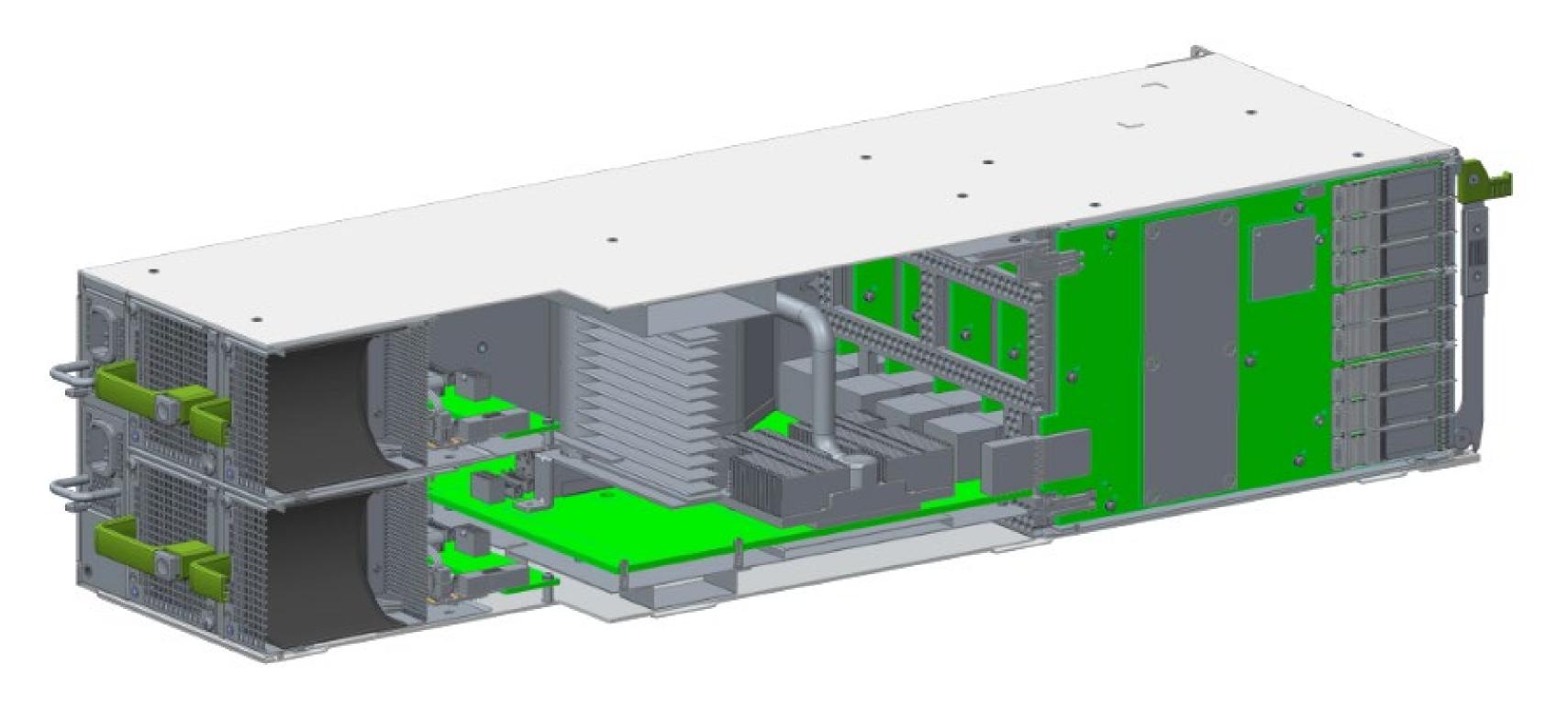








Side Cross-Sectional View







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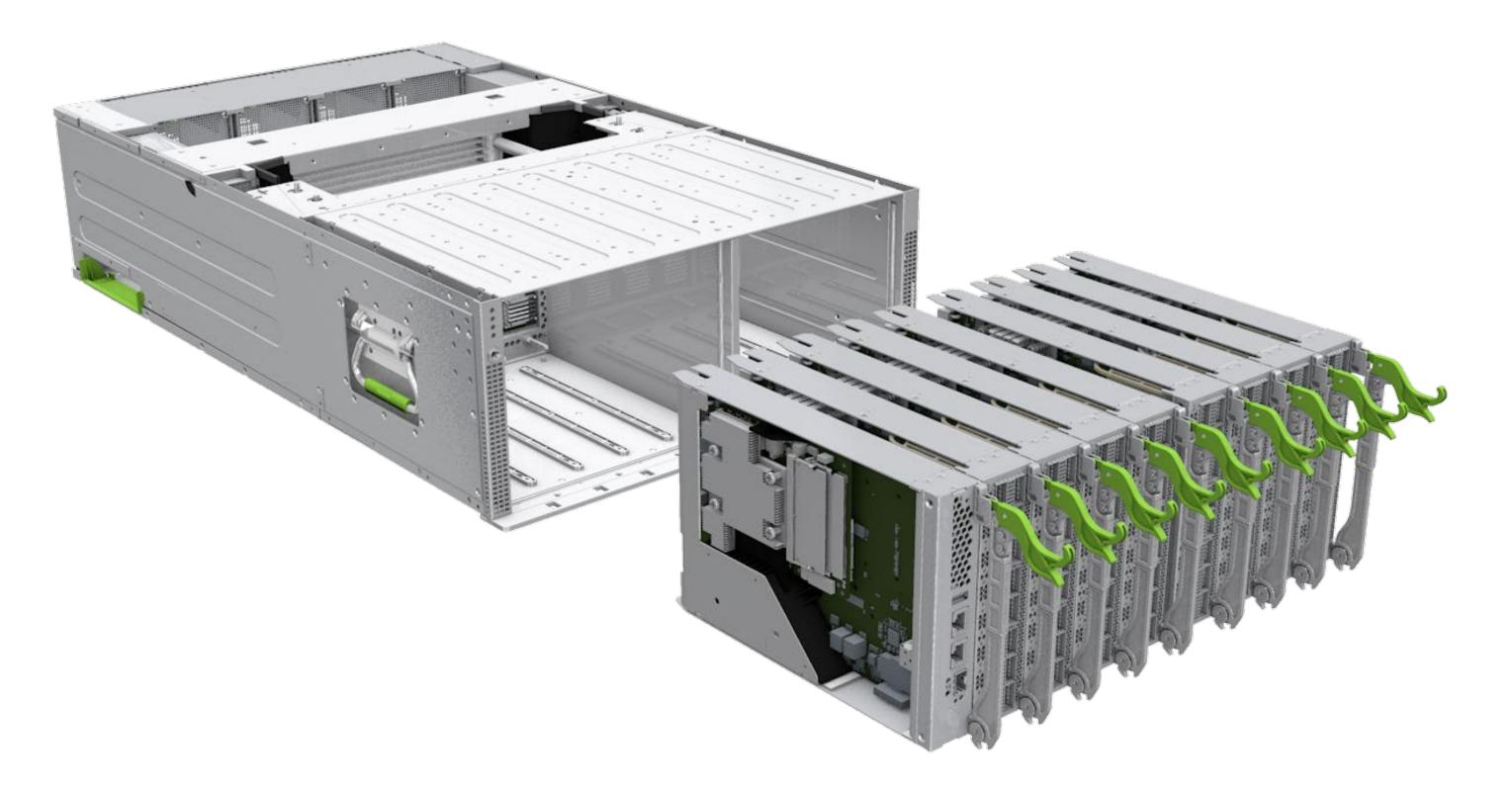








• PIM and SCM slide forward from the chassis







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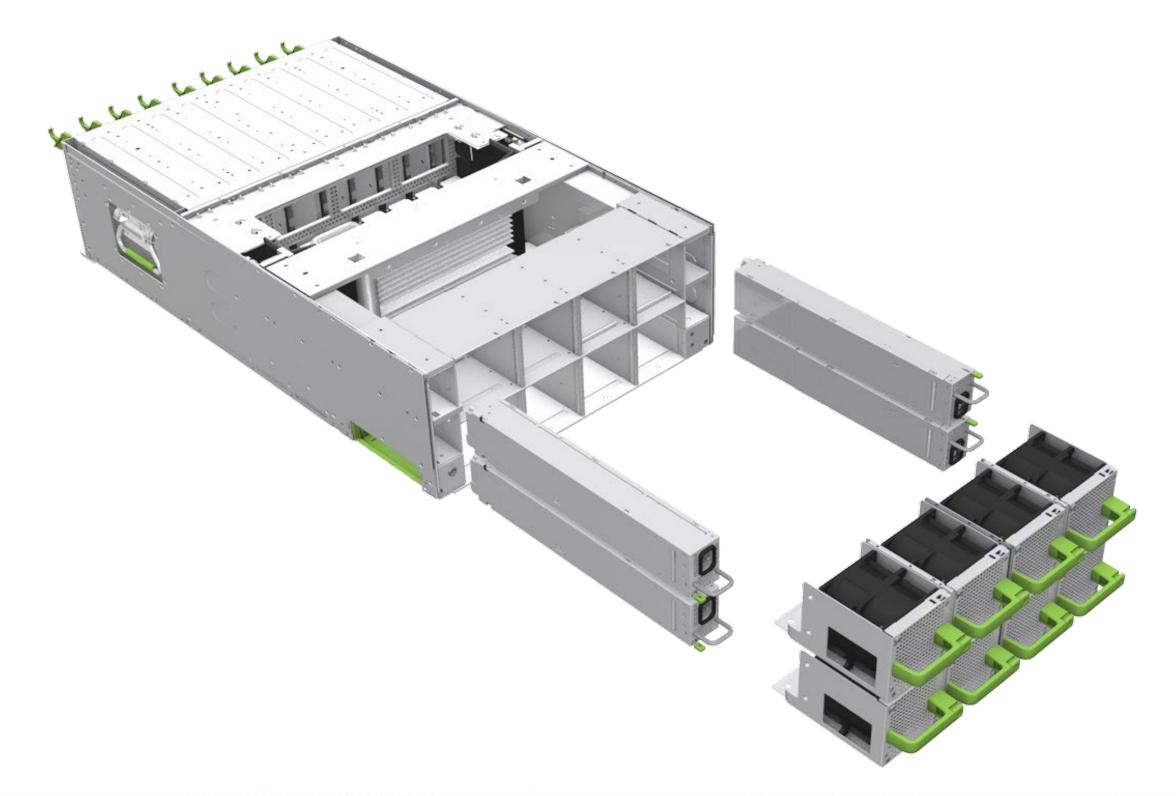








PSU and Fan module slide outside from the chassis







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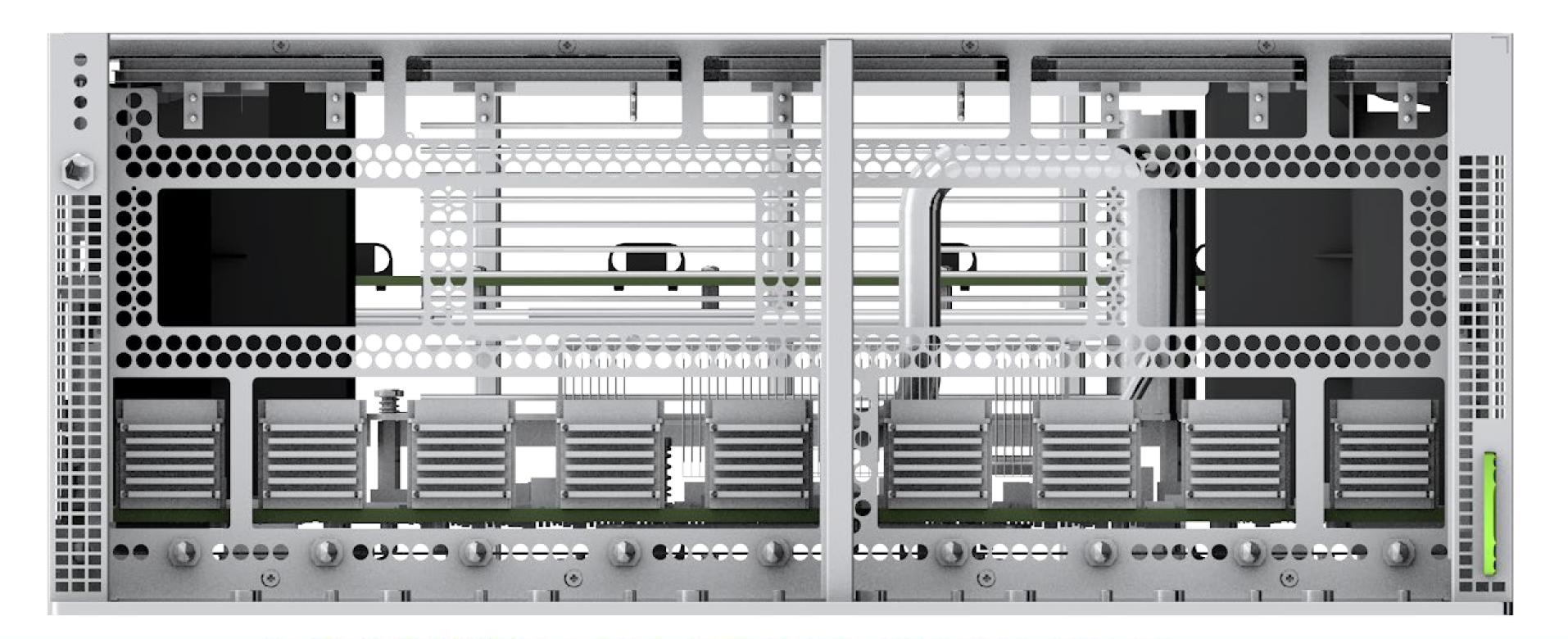








Showing front to back vent opening





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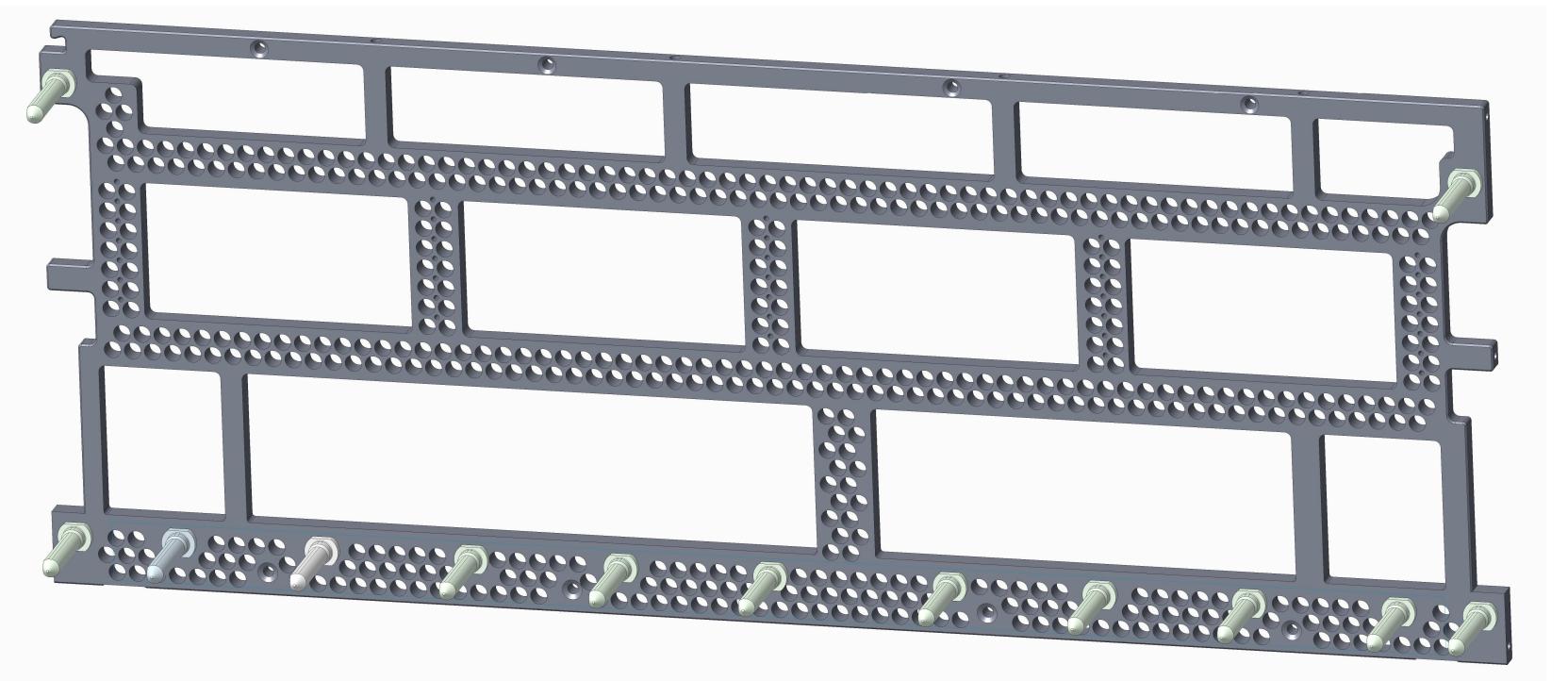








 Metal midplane design to allow maximum airflow to past thru the chassis







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Innovative Design

- Disaggregated architecture:
 - Field Replaceable SCM(System Control Module)
 - Field Replaceable PIM(Port Interface Module)
- Orthogonal Direct Chassis Architecture

 - All major module cards are designed to be mated orthogonally - Open up more air channel for a better thermal performance - Reduce the PCB trace length for better signal integrity
- A sophisticated thermal design to support low cost 55C CWDM4 optics







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Minipack System Major Components

- Switch Main Board (SMB)
- System Control Module (SCM)
- Port Interface Module (PIM)
- Fan Control Module (FCM)
 - Fan Control Module Top (FCM-T)
 - Fan Control Module Bottom (FCM-B)
 - Power Distribution System
 - Horizontal Bus Bar (HBAR)
 - Power Distribution Board Left (PDB-L)
 - Power Distribution Board Right (PDB-R)



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- PIM-16Q: Port Interface Module with 16 x QSFP28 100G









Minipack Chassis: 128 x 100G







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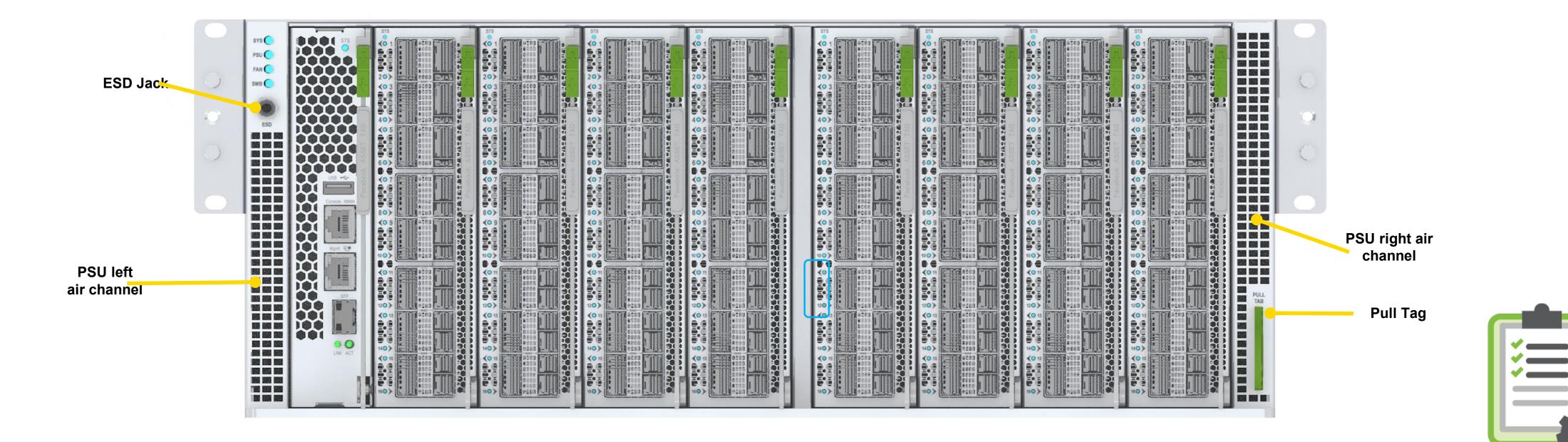


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Minipack Front View with PIM-16Q







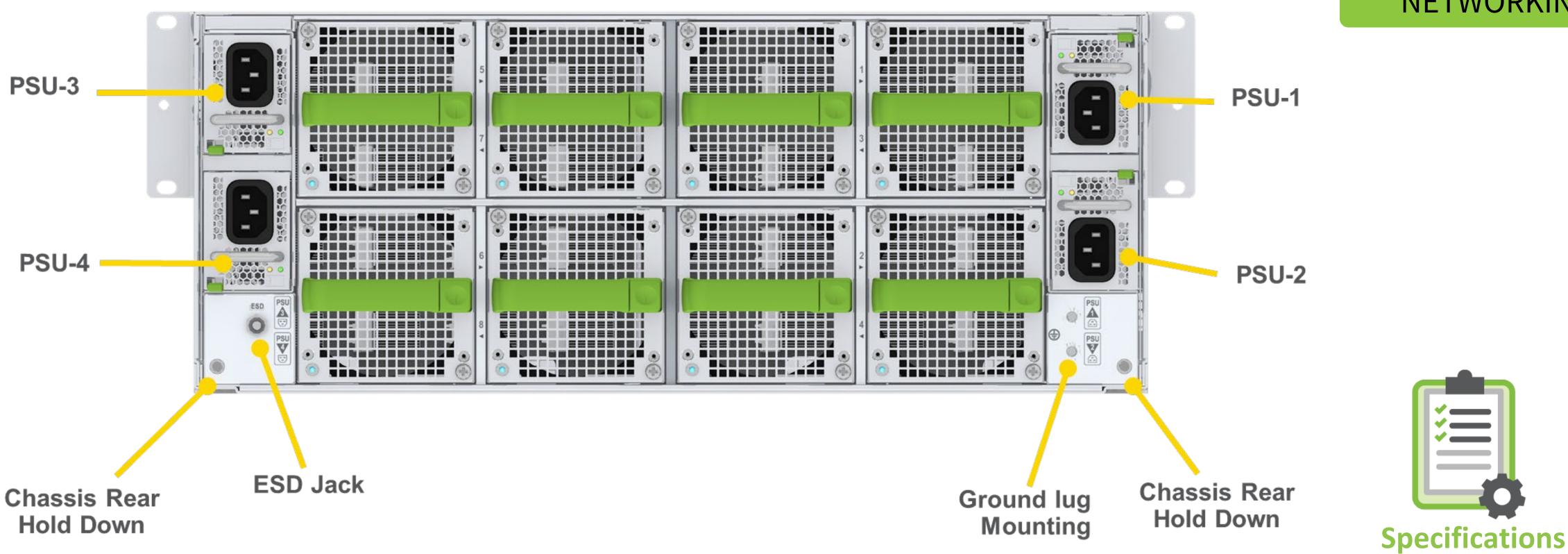


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Minipack Rear View





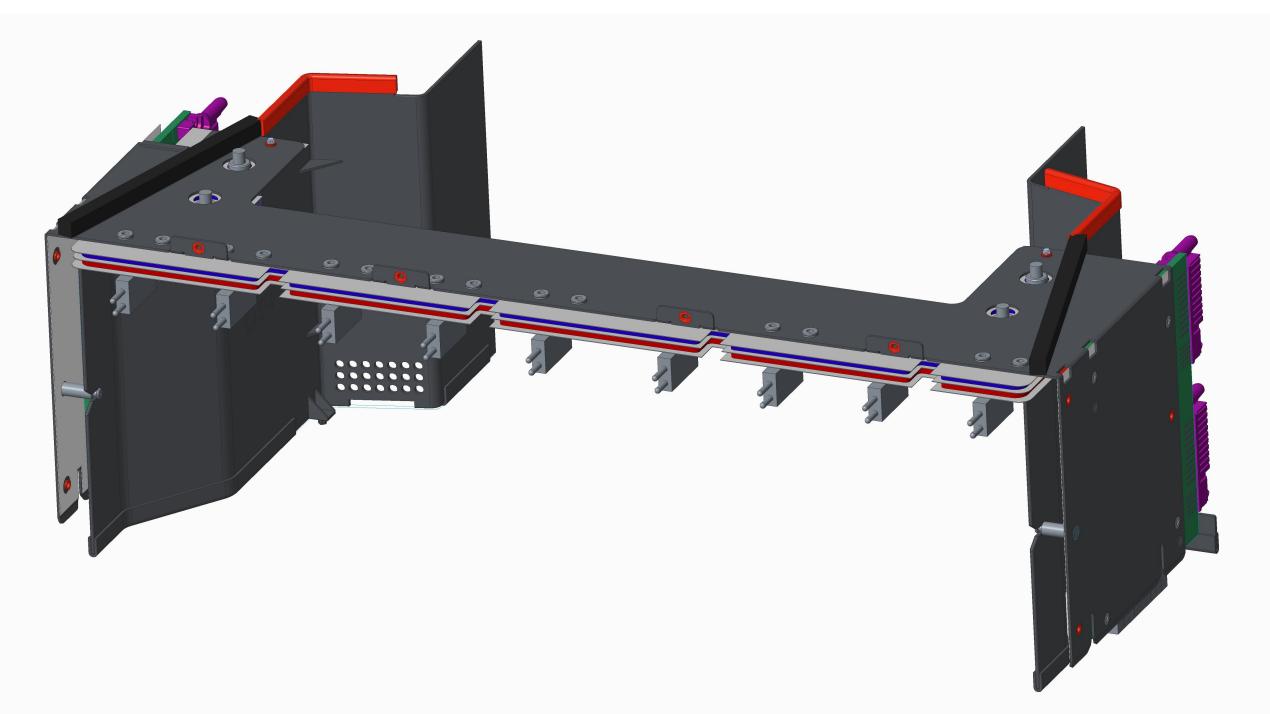






Power Distribution System

- Two layer busbar assy
- PDB-Left and PDB-Right







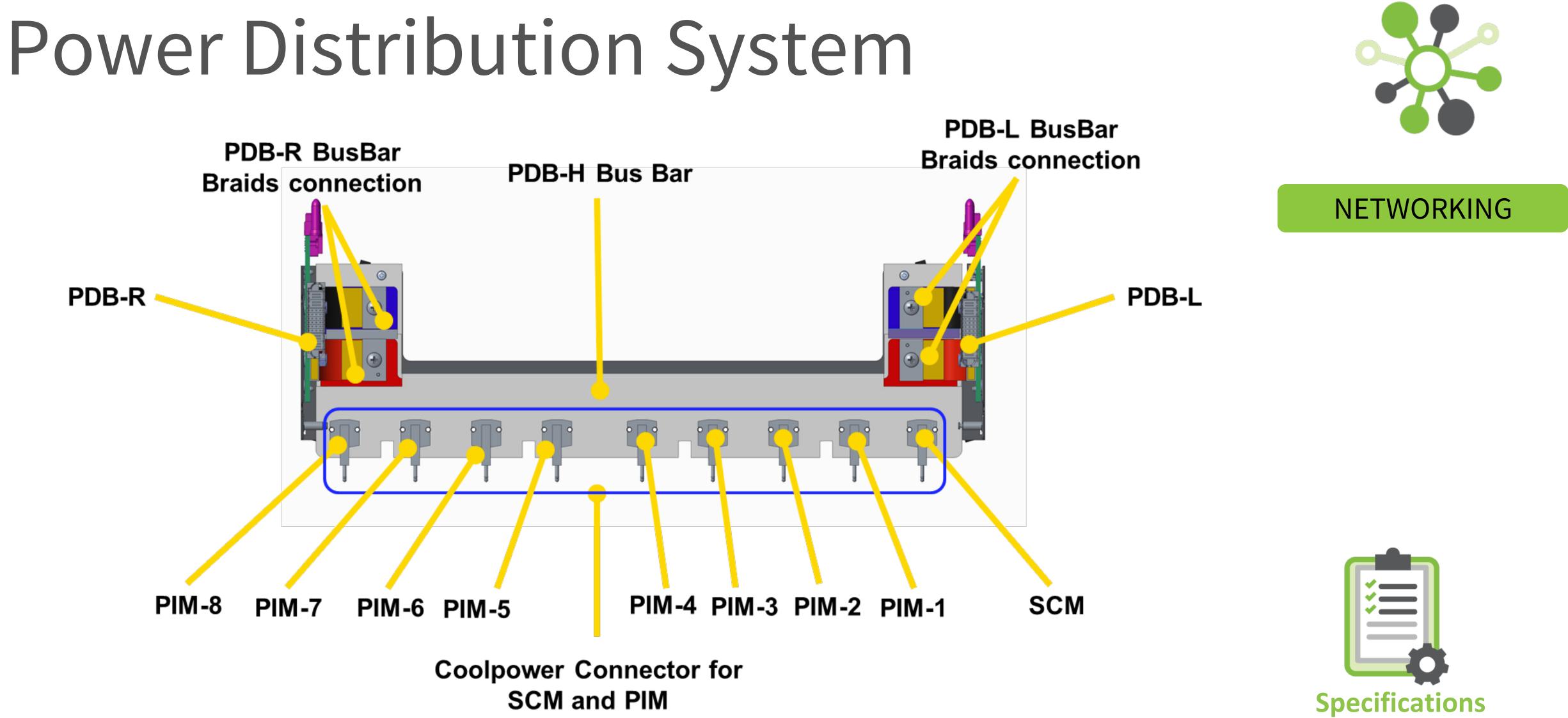










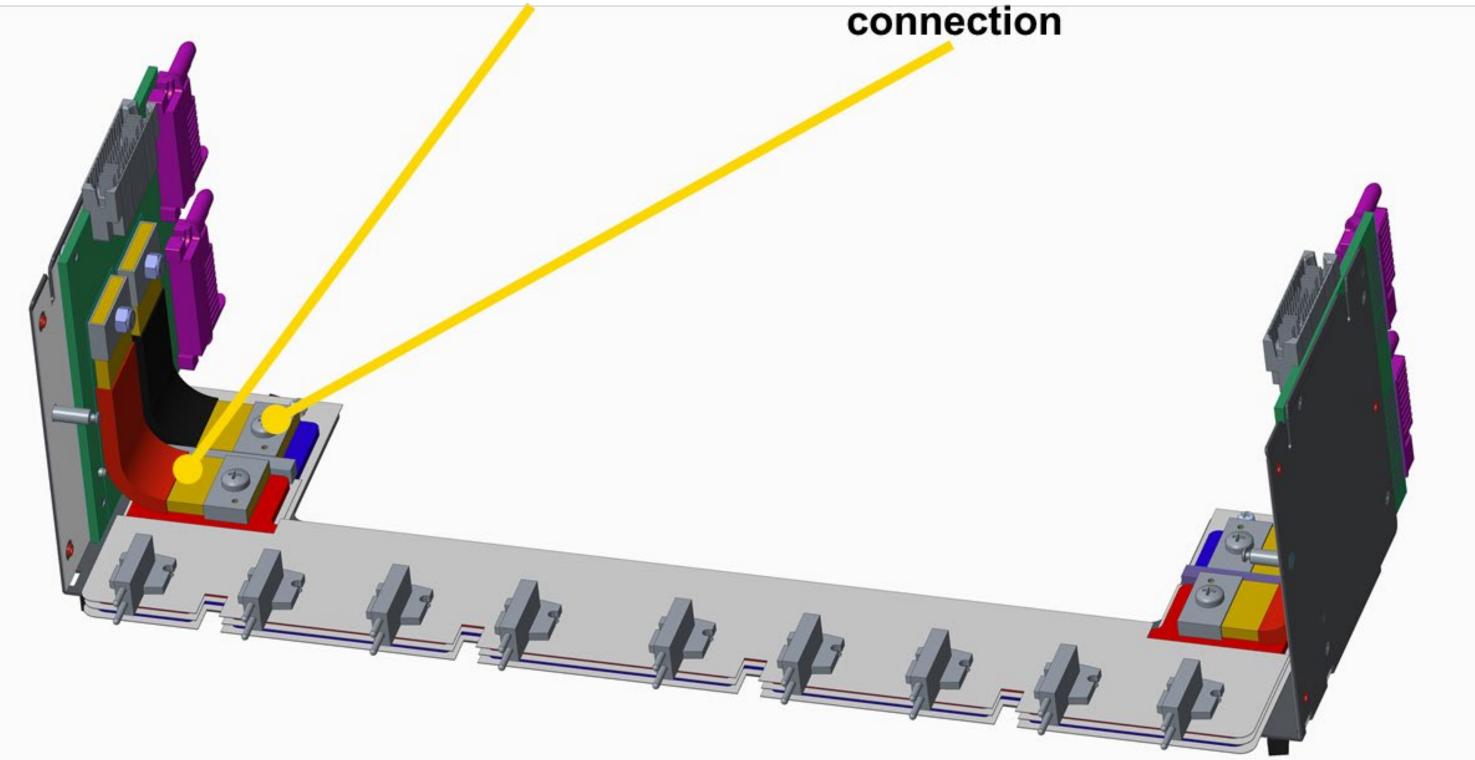






Power Distribution System

PDB-R BusBar Braids Power connection







PDB-R BusBar Braids Ground



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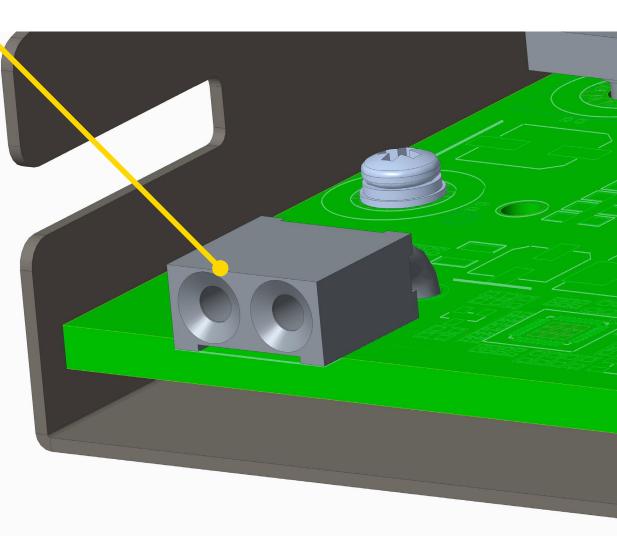




Power Distribution System

CoolPower connector also an Orthogonal Connector

CoolPower Module side connector

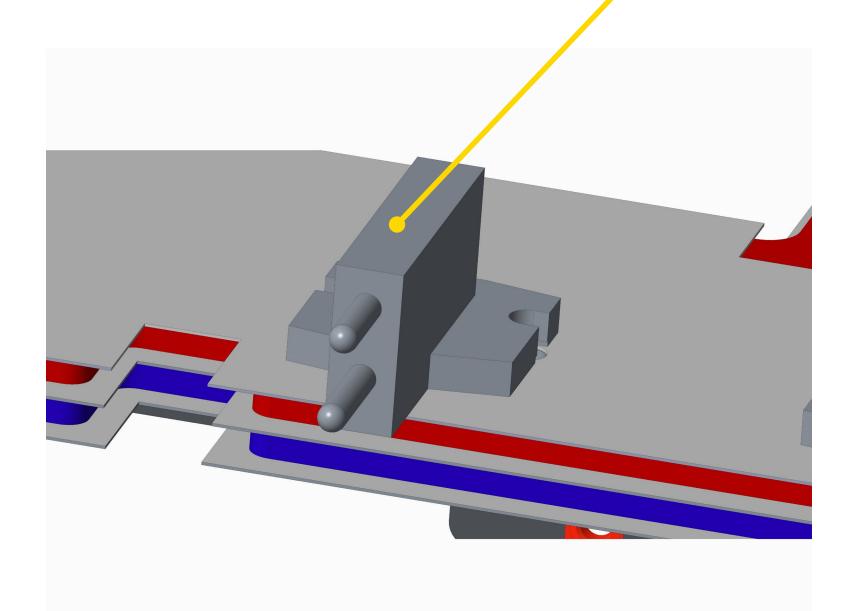








CoolPower Busbar side connector









FRUs

- Front of the chassis
 - SCM(System Control Module)
 - PIM(Port Interface Module)
- Rear of the chassis
 - Fantray assy
 - PSU





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System Control Module (SCM)

COMe module • Minilake SATA M.2 SSD





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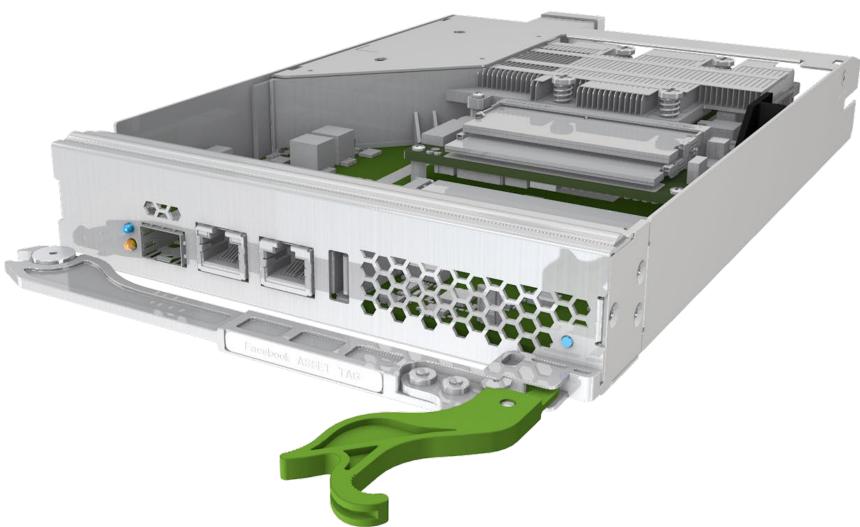
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System Control Module (SCM) NETWORKING Key to prevent inserting in 254 wrong slot a beite fin fint ? - mun ? (1) (0) **~** _____ ***** (Unit : mm) 168 **Specifications**





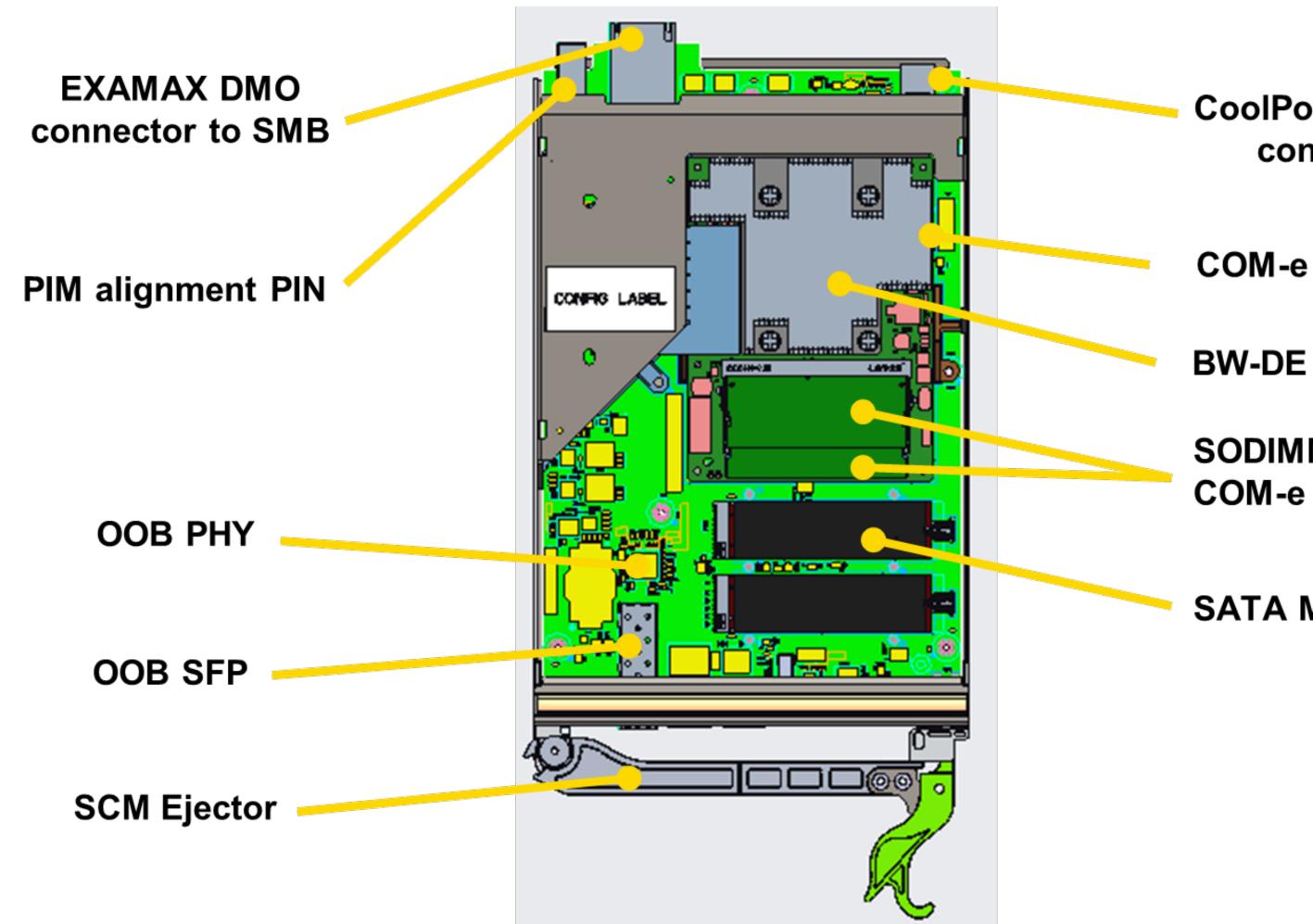








System Control Module (SCM)







CoolPower power connector



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BW-DE CPU

SODIMM on COM-e Module

SATA M.2 SSD







Port Interface Module (PIM)

Type of PIM • PIM-16Q: 16 x QSFP28

One DMO Connector to SMB for both data and control plane signals

CoolPower connector to power bus bar







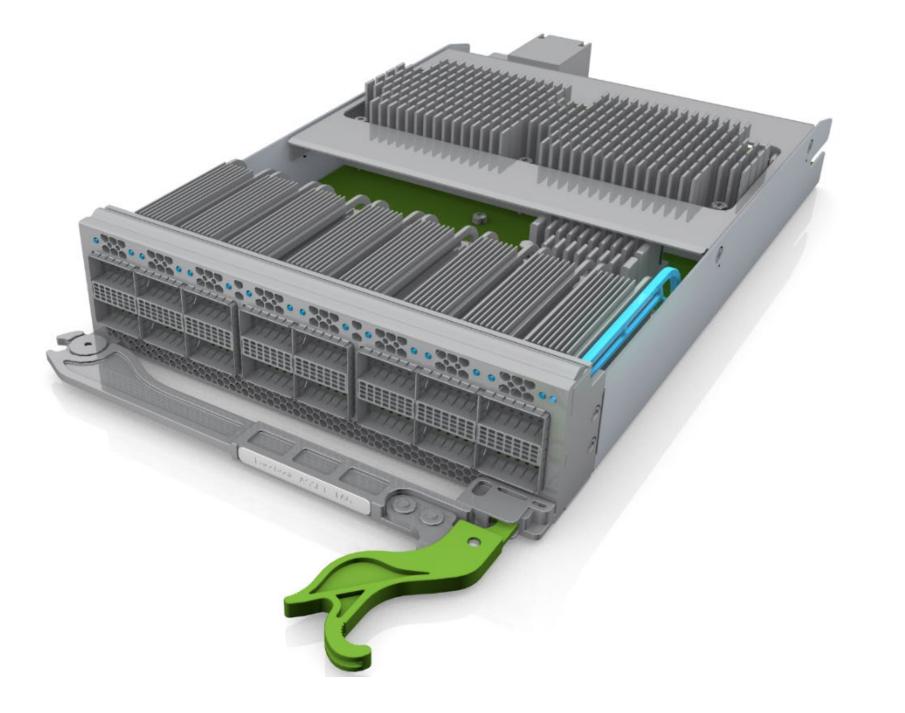
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PIM-16Q Module







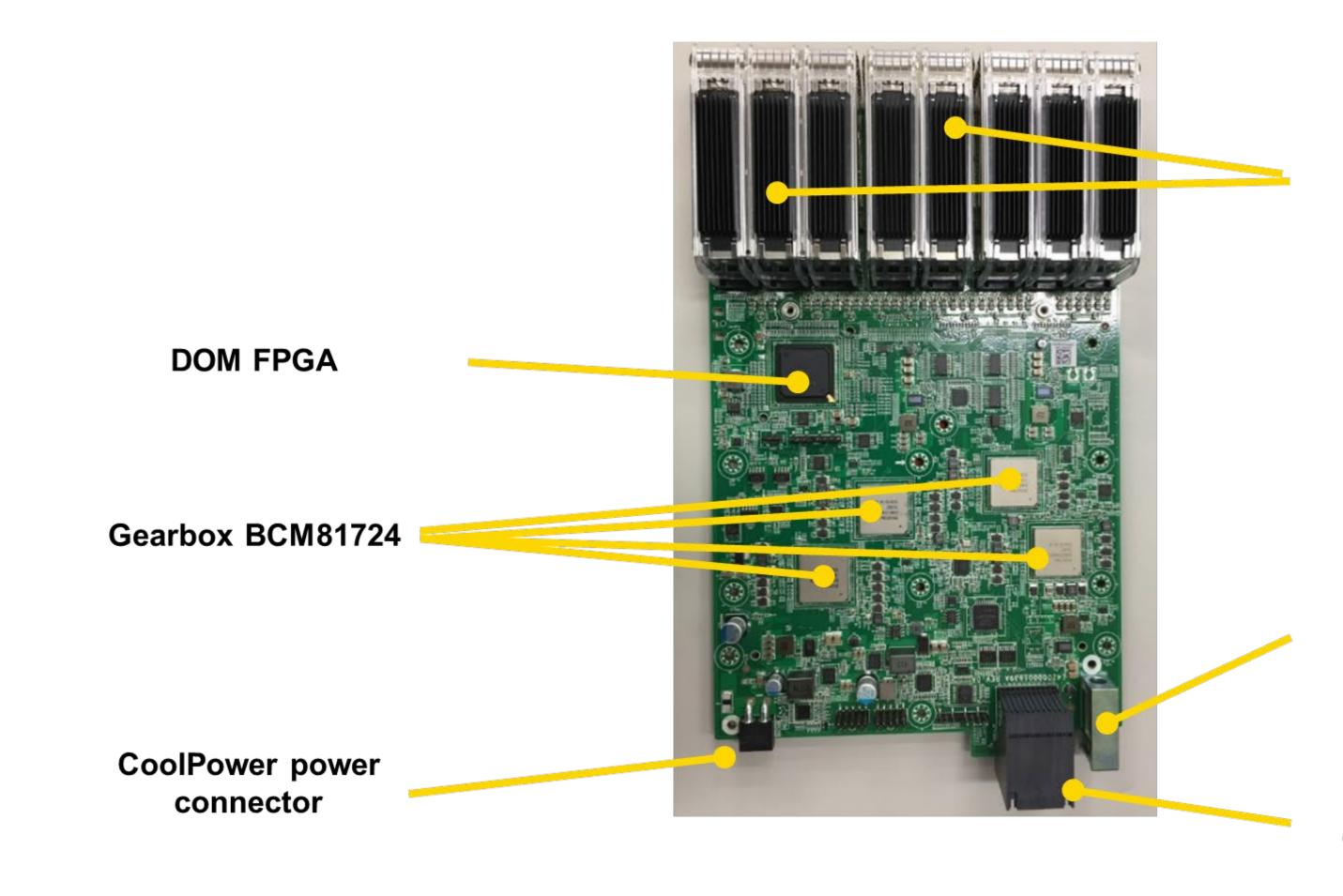
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PIM-16Q Components







16 QSFP28 connector

EXAMAX DMO connector to SMB

PIM alignment

PIN

~ Specifications









PIM-16Q interface

Four BCM81724 **Gearbox PHYs share** one heatsink

Gearbox Heatsink

CoolPower power connector





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EXAMAX DMO connector to SMB









16 QSFP28 connector







Fan-tray Assy

- Screw-less latch design for easy maintenance
- Powerful 80 x 80 mm CR fan
- Hot swappable
- LED on rear panel
- Each FCM carries 4 Fan-tray, total 8 fan-tray in minipack chassis







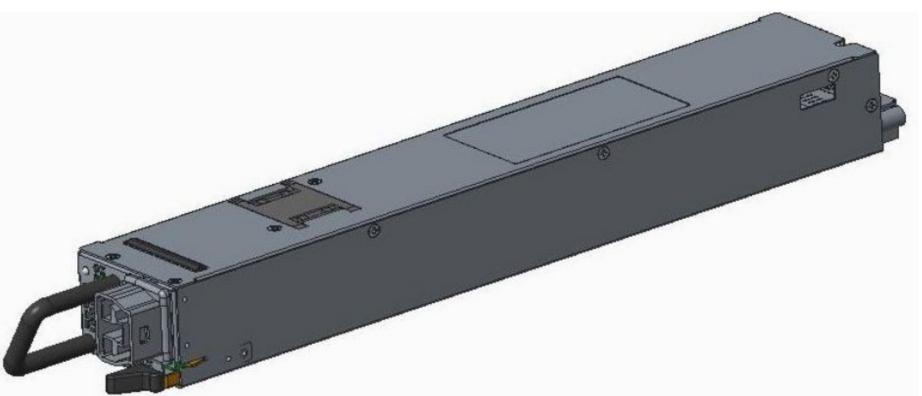






High Performance PSU

- Keying feature on Minipack chassis to prevent PSU to mate in wrong direction
- Power cord retention is built in on the HVAC connector

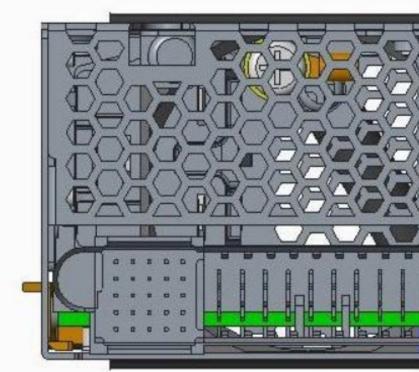






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Specifications









Thermal Design

- Front-to-rear airflow
 - Ensures N+1 rotor redundancy \bigcirc
- Supports 100G modules at 3.5W with 55°C case temperatures
 - Up to 35°C ambient, 6000ft altitude and under one rotor failure 0
- Fan control delivers desired margins on all temperature-sensitive components and system-level ΔT targets
 - Under typical and high workloads \bigcirc







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Thermal Design

- Separation between system and PSU airflow channels
- Other items
 - Energy-efficient fan operation (typically $\leq 5\%$ of system power) \bigcirc
 - System-level ΔT targets ensure efficient operation in deployed Ο facilities
 - Thresholds defined for critical parameters to sustain operation under \bigcirc unexpected conditions







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Call to Action

your experiences with the community.

 Please visit OCP website to learn more on MiniPack design https://www.opencompute.org/wiki/Networking/SpecsAnd **Designs#Facebook_Minipack**





• MiniPack design is available for your use. Please share









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OCP Regional Summit 26–27, September, 2019

