Open. Together.
Project Olympus 3U PCIe Expansion Server Deep Dive

Raymond Miles, VP Architecture, ZT Systems
Mark D. Chubb, Director Architecture ZT Systems
Agenda

ZT Systems Overview
Project Olympus Building Blocks
ZT 3U PCIe Expansion Server Products
Chassis
Flexible Riser Card Options
Power Distribution Board
Slide Rails
ZT Systems Overview

- Leading provider of optimized servers and storage servers for hyperscale data centers
  - Founded in 1994
  - HQ in Secaucus, NJ; global sites & capabilities
  - Private company

- Built for Hyperscale
  - Maximum design flexibility
  - Continuity of Supply
  - Industry leading achievement to aggressive delivery SLAs
  - Full range of flexible service and support options
  - Broad range of component options

A solution design and manufacturing partner with a record of success serving the world’s largest data center customers
ZT OCP Engagement

- History
  - Engaged since 2012
  - Gold Member since 2014
  - Engineering projects
  - Solution Provider
  - Sponsored 8 summits

- Products
  - 2U Open Storage (2014)
  - 1U XPO200 (2017)
  - XPO200-3UN PCIe Expansion System (2019)
Project Olympus HW Building Blocks

- **Motherboards**: Flexible expansion
- **Power Supply**: 1KW three-phase
- **Universal PMDU**: AC power cord adaption
- **Rack**: 19” EIA-310 standard
- **Rack Manager**: Restful API or Redfish via Ethernet

Project Olympus is an OCP Accepted™ Specification
3U PCIe Expansion Server

**Project Olympus Rack**
- Fully Compatible

**Intel® SKU**
- 3 x Load Balanced 1kW PSUs
- Power Distribution Board (PDB)
- PSU blind mate into Rack PMDU
- 6 x 60mm Dual Rotor Fans (room for 8 Fans)
- 2.5”/3.5” SSD or HDD
- Supports Slide Rail Kit for smoother travel

**AMD® SKU**
- 5 x FHFL, Double-wide 300W x16 PCIe Cards + 1 x FHHL Single-wide 75W x16 PCIe Card

**OR**

**Intel® SKU**
- 6 x FHFL, Double-wide 300W x16 PCIe Cards + 1 x FHHL Single-wide 75W x16 PCIe Card

**AMD® SKU**
- 12 x FHFL, Single-wide 75W x16 PCIe Cards + 1 x FHHL Single-wide 75W x16 PCIe Card

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Open. Together.
# ZT Solution: 3U PCIe Expansion Server
(AMD® EPYC™ Based; ZT-XPO200-3UA)

<table>
<thead>
<tr>
<th>Feature</th>
<th>Qty</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Chassis</td>
<td>1</td>
<td>3U, 19” EIA310-D Compliant supporting Project Olympus PMDU connections</td>
</tr>
<tr>
<td>Motherboard</td>
<td>1</td>
<td>Project Olympus 2-Slot AMD® EPYC™ Motherboard (OCP leveraged) from ZT Systems</td>
</tr>
<tr>
<td>Processor</td>
<td>2</td>
<td>AMD® EPYC™ 7551 (Naples), 180W, 32C, 2GHz</td>
</tr>
<tr>
<td>Memory</td>
<td>16 (32max)</td>
<td>32GB DDR4, DR (1DPC), 2667 R-DIMM; Total System Memory: 512GB</td>
</tr>
<tr>
<td>PCIe Riser 3 &amp; 5</td>
<td>2</td>
<td>2-Slot Active PCIe x16 Riser Card with 48-lane PCIe Switch</td>
</tr>
<tr>
<td>PCIe Riser 4</td>
<td>1</td>
<td>2-Slot Passive PCIe x32 Riser Card</td>
</tr>
<tr>
<td>GPU Card</td>
<td>4 (5 max)</td>
<td>AMD® MI25 FHFL, Double-Wide, 300W PCIe x16 Card</td>
</tr>
<tr>
<td>Network Card</td>
<td>1</td>
<td>10G Single Port SFP+ PCIe2.0 x8 5GT/s</td>
</tr>
<tr>
<td>HDD/SSD</td>
<td>5 (8 max)</td>
<td>M.2 960GB NVMe SSD, PCIe x4 110mm</td>
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<tr>
<td>M.2 Riser</td>
<td>1 (2 max)</td>
<td>2-Slot M.2 Riser (in PCIe Slot 1)</td>
</tr>
<tr>
<td>Security</td>
<td>1</td>
<td>TPM2.0 SPI Module</td>
</tr>
<tr>
<td>System Fans</td>
<td>6</td>
<td>60mmx56mm Dual Rotor Fan</td>
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<tr>
<td>Power Supply</td>
<td>3</td>
<td>Project Olympus 1020W 3-Phase, non-LES PSU</td>
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<td>PDB and Cable Harnesses to support 12V Power to MB, Risers, GPUs, and System Fans</td>
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*Items in Orange are being provided to OCP by ZT Systems*
## ZT Solution: 3U PCIe Expansion Server
(Intel® Xeon®-SP Based; ZT-XPO200-3UN)

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<td>2</td>
<td>Intel® Xeon® Platinum 8168 (Skylake), 205W, 24C, 2.7GHz</td>
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<tr>
<td>Memory</td>
<td>12</td>
<td>32GB DDR4, DR (1DPC), 2667 R-DIMM; Total System Memory: 384GB</td>
</tr>
<tr>
<td>PCIe Riser 3 &amp; 5</td>
<td>2</td>
<td>4-Slot Active PCIe x16 Riser Card with 96-lane PCIe Switch</td>
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<tr>
<td>PCIe Riser 4</td>
<td>1</td>
<td>5-Slot Active PCIe x16 Riser Card with 96-lane PCIe Switch &amp; Sliver to Edge Conn x8 Cables</td>
</tr>
<tr>
<td>GPU Card</td>
<td>12</td>
<td>Nvidia® P4 Low Profile, Single-Wide, 75W PCIe x16 Card</td>
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3U PCIe Expansion System Options

ZT Systems: ZT-XPO200-3UA

ZT Systems: ZT-XPO200-3UN

ZT Systems in process of submitting these 3U Server Design Specifications to OCP Community!
2-Slot Active PCIe x16 Riser Card Layout

2 x PCIe x16 slots supporting (Slots #1-2, Bottom to Top):
- 2 x FHFL Double-Wide PCIe cards
- Requires PWRBRK_N (pin B30) signal for High Power PCIe Card throttling

Broadcom® 48-Lane PCIe Gen3 Switch (PEX8747)
- Switch Bifurcated into 2 x16 Ports (1 x16 Port to each x16 Slot)
- Switch is located on Backside as required by Chassis Thermals (Heatsink is required)
- Use of all 48-lanes and requires 15.5W

Differential Clock Buffer on board for additional Clock Fanout

I2C MUX used to avoid Address contention (see I2C block diagram)

8 Layer Stackup, Mid-Loss Material (0.062”)

Enclosed in a Riser Cage for easy Serviceability and Retention
2-Slot Passive PCIe x32 Riser Card Layout

For Project Olympus AMD® EPYC™ MB Only (Middle Riser)
Uses 2 x HSEC8 connectors in-line for x32 Lanes

1 x PCIe x16 slot supporting (Slot #1, Bottom):
- 1 x FHHL Single-Wide PCIe card
- Designated for the Network Card

1 x PCIe x16 slot supporting (Slot #2, Top):
- 1 x FHFL Single-Wide PCIe Card OR
- 1 x FHFL Double-Wide PCIe Card
- All x16 PCIe lanes sourced from PCIe Riser Slot #4 (No Switch Required)
- Requires PWRBRK_N (pin B30) signal for High Power PCIe Card throttling

I2C MUX used to avoid Address contention (see I2C block diagram)

8 Layer Stackup, Mid-Loss Material (0.062”)
Other 3U Server PCIe Riser Card Options

2-Slot Passive PCIe x16 Riser Card

3-Slot Active PCIe x16 Riser Card

4-Slot Active PCIe x16 Riser Card

5-Slot Active PCIe x16 Riser Card

Provides PCIe Expansion System Flexibility!
Middle PCIe Riser Card connections

- Sliver Connector to PCIe x8 Edge Connector Cable
  - Provides additional PCIe Lane connections from unused PCIe Slots
  - 2 Cables provide PCIe x16 Lanes to Riser PCIe Slot or Riser PCIe Switch
  - Supports Middle Riser Options:
    - 2-Slot Passive x16 Riser Card
    - 3-Slot Active x16 Riser Card
    - 5-Slot Active x16 Riser Card
  - Typically used on the Intel® Xeon®-SP System SKU
  - ZT Partnered with Tyco Electronics® on design
**PCIe I2C Block Diagram**

All PCIe Connectors connect to the BMC I2C for PCIe Card telemetry.

- Use the PCIe designated pins for I2C (B5 & B6).
- To avoid I2C Address contention between PCIe Cards, an I2C MUX is needed on each riser
- The design must add appropriate voltage translation/isolation between different I2C voltage domains.

**Notes:**
- M.2 Slots labeled #1-4 in MB Scren.
- M.2 I2C Addresses (VPD: 0xA6; Mgmt: 0x3A)
- 0-based int

**Riser 1, PCIe x8**

**Riser 2, PCIe x8**

**Riser 3**

**Riser 4 (x32)**

**Riser 5**

**Bus Naming:**
- 1-based pin
- 0-based int

**Physical Slot #'s**
- Slot 3.4
- Slot 3.2
- Slot 4.2
- Slot 4.1
- Slot 5.4
- Slot 5.2
- Slot 6.4
- Slot 6.2
- Slot 7.4
- Slot 7.2
- Slot 8.4
- Slot 8.2
- Slot 9.4
- Slot 9.2
- Slot 10.4
- Slot 10.2
- Slot 11.4
- Slot 11.2
- Slot 12.4
- Slot 12.2

**Notes:**
- M.2 Slots labeled #1-4 in MB Scren.
- M.2 I2C Addresses (VPD: 0xA6; Mgmt: 0x3A)
PCIe Clock Block Diagram

Differential Clock Buffers used on the Risers to fanout additional PCIe Clocks.

The Clock Buffers are set to PLL Bypass Mode by default (shown to provide best margins)

Included resistor stuffing Options to change the PLL Bandwidth Mode if needed to adjust Clock Jitter performance:

- PLL High BW Mode
- PLL Low BW Mode
- PLL Bypass Mode (default)
Power Distribution Board Layout

PDB supports single 12V Power Domain from 3 x Project Olympus PSUs
- Uses Passive Droop Sharing
- No V_Sense or I_Share signals used

Power Delivery Requirements
- Hot Swap Controller for Power Monitoring and Isolation
- 12V Current Sense Ckts used for each branch
- Supports Power to entire System (MB, Risers, PCIe Cards)
- Fan Power also comes from PDB (none from MB)

Provides passthrough of PMDU signals between MB and RM

6 Layer Stackup (0.093"), 2oz copper planes

Different power harnesses can be used for different High-Power PCIe Cards
PDB Power Distribution Block Diagram

- **PSU1**: 0xB0, 2x12
- **PSU2**: 0xB0, 2x12
- **PSU3**: 0xB0, 2x12
- **Hot Swap Controller (ADM1278)**
- **Riser 3 (2-Slot)**: 2x6
- **Riser 4 (2-Slot x32)**: 2x2
- **Riser 5 (2-Slot)**: 2x6
- **Note**: Motherboard has its own Hot Swap Controller & Switch Ckt.

- **Hot Swap Controller (ADM1172)**
- **Gen5 Riser P3V3_CB VR**
- **P3V3_STBY VR**
- **P3V3_RISER SW**
- **Fan Power (6 x 60mm)**
- **1U Fan Power & HDD**
- **HSC/ SW**

**Rev 1.2**
Provides passthrough of PMDU signals between MB and RM
3U Server Slide Rails

Supports Slide Rails for smoother travel during servicing

Due to the weight of C2310 Server (90lbs), standard Project Olympus T-Pin Slide Rails will not allow the Chassis to pull in and out smoothly.

Width of Chassis prevents thicker rail slides (only 67% pull-out)

Top cover is split into two sections (Front and Rear).

Front Top Cover section allows access to most of the System hardware, leaving only the System Fans, PDB and Power Cables under the Rear Top Cover without in-Rack access.

Using Slide Rails from King Slide®
Plans for OCP Recognition of Project Olympus 3U Server

3U Server Base Spec submitted by Microsoft® (Siamak Tavallaei) – Targeting OCP Acceptance by Mid 2019

3U AMD® EPYC™ Server Product Spec & Design Collateral submitted by ZT – Targeting OCP Acceptance by Q3 2019

3U Intel® Xeon®-SP Server Product Spec & Design Collateral submitted by ZT – Targeting OCP Acceptance by Q3 2019

Both Products to be Available on Open Compute Marketplace 2H 2019

Visit us at Booth A17
Or www.ztsystems.com/ocp