Server Tech Talk Day 1 Chat Transcript
April 26, 2022

00:45:20 Kate Hendle: Welcome to the first OCP Server Tech Talk! Feel free to put any questions for the speakers here in the chat box!

00:56:20 Ravi B: Will the future spec be updated to enhance coverage on hot swap design?

00:56:43 Jeff Casazza: Where will these talk recordings be available, as I am unable to attend the entire agenda?

00:57:15 Michael Schill: Yes - all tech talk recordings will be posted here: https://www.opencompute.org/events/past-events


01:20:38 Shohei: Question about OAM input voltage. Even as AUX voltage, is 12 voltage no longer available in OAM 2.0?

01:25:20 Whitney Zhao: for question 1 above, the answer is in this slide from NVIDIA contribution.

01:26:53 Song Kok Hang: PCIe 6 in OAM would be the ASIC or GPU. For 800G DAC cable...what we got from one of the cable vendor is 1.5 meter. They are actively working on improving the loss.

01:27:22 Song Kok Hang: 12V is removed from OAM v2.0.

01:27:32 KWANG04: Are you considering to enlarge the SXM format to be more compatible with OAM?

01:27:52 KWANG04: In your next gen modules.

01:29:53 KWANG04: For the OAM2.0, is there a plan to a more detailed mechanical spec?

01:34:57 Douglas: For OAM Mezz connector, can the MPN be shared?

01:41:53 Song Kok Hang: You can find the MPN in OAM spec.

01:45:05 Mark Hemmeyer: do you have the water temperature range and flow?

01:51:44 Peter Chen: Water Temperature has been tested up to 45C. Flow up to 4L/min for each node.

02:27:46 Kate Hendle: Please input any questions for the speakers here.

03:16:21 Jaime Edwards - AMD: How does the team see UCIE alignment happening?

03:16:27 aho4: will the presentation and/or video be posted later?

03:16:41 Kali Burdette (OCP): Yes, we are recording this and will post the video and slides on the OCP website.

03:16:54 Kali Burdette (OCP): https://www.opencompute.org/events/past-events

03:18:29 Sandeep.Mendiratta: Thanks for the link.

03:19:37 Jaime Edwards - AMD: Thank you for that answer.

03:20:10 sheng: What is the max number of chiplets per socket? Or what is the max socket dim?

03:21:08 sheng: 700W: max dimension a socket?

03:22:34 sheng: Given 1U 700W board, should I choose one socket with N chiplet? or N sockets with M chiplets?

03:23:30 sheng: Thanks.

03:23:30 Michael Schill: ODSA Calendar can be seen here: https://www.opencompute.org/projects/open-accelerator-infrastructure

03:25:31 Chris Schmolze: Are you able to share tomorrow's agenda now?
Thanks John and all the presenters. Great material today!

Agenda is on the Eventbrite site for this event.