



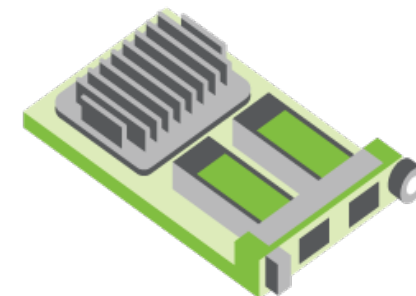
OCP NIC3.0 ACTIVITIES & NEXT-GEN UPDATES

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Jason Rock, Dell-EMC

Connect. Collaborate. Accelerate.

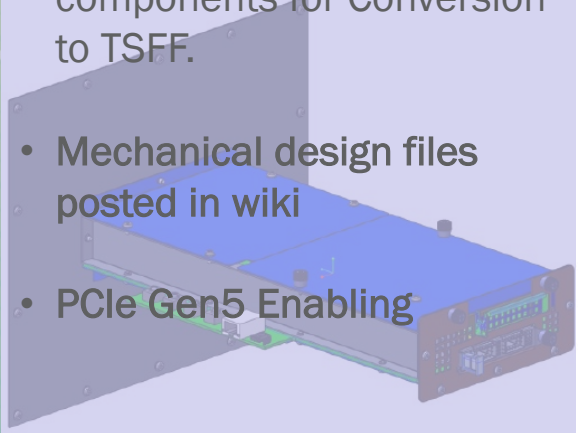

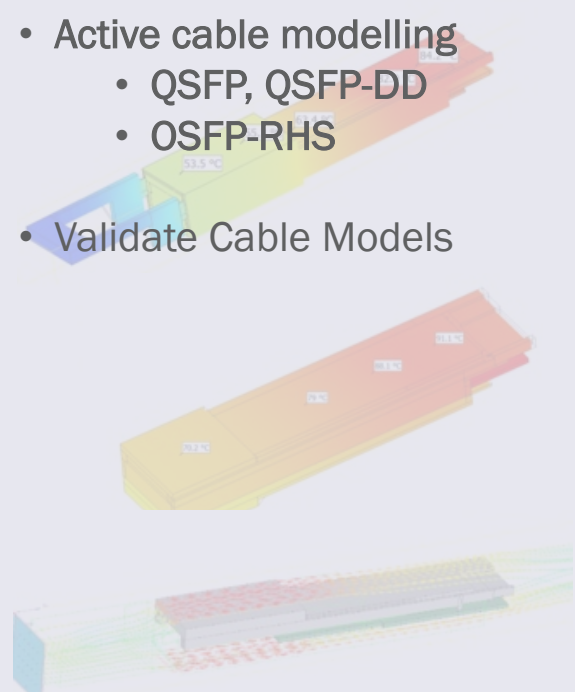
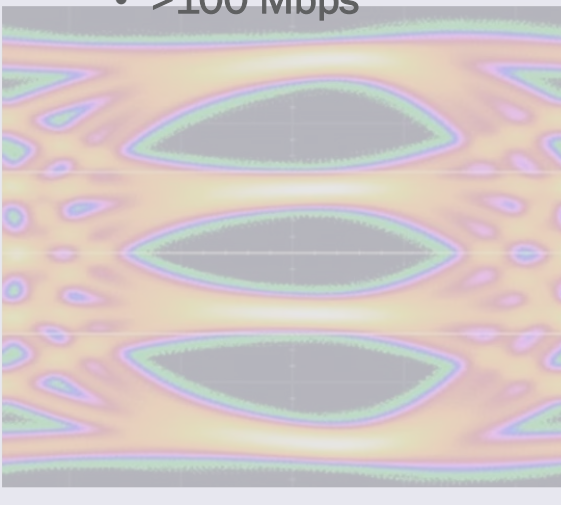


SERVER



NIC3.0

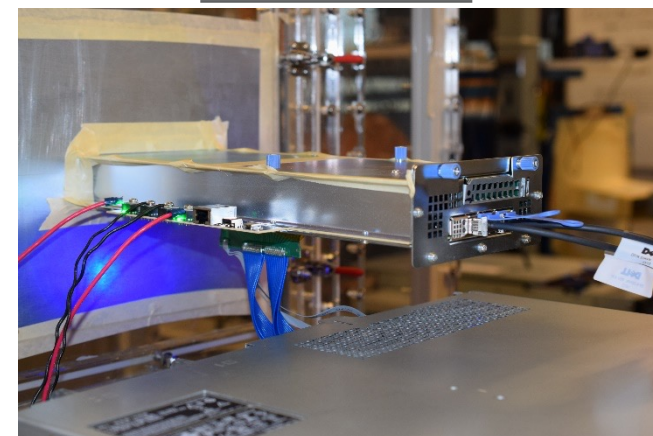
NIC Sub-group activities

TSFF Thermal Fixture	PCIe Gen 5.0 SI Fixture	Thermal Modelling	Next-Gen
Taller Faceplate, Increased Cooling Solutions	System & OCP NIC 3.0 Compatibility	Common reference model	Speed Bump
<ul style="list-style-type: none"> Re-Use of Existing Thermal Test Fixture. 3 Different components for Conversion to TSFF. Mechanical design files posted in wiki PCIe Gen5 Enabling 	<ul style="list-style-type: none"> Compliance Load Board (CLB) <ul style="list-style-type: none"> In Lab Compliance Base Board (CBB) <ul style="list-style-type: none"> Design soon 	<ul style="list-style-type: none"> Active cable modelling <ul style="list-style-type: none"> QSFP, QSFP-DD OSFP-RHS Validate Cable Models 	<ul style="list-style-type: none"> PCIe Gen6 RMII based Transport (RBT) <ul style="list-style-type: none"> >100 Mbps 

PCIe® 5.0 Thermal Test Fixture (TTF)

- The TTF was designed to support speeds up to PCIe® 5.0 32GT/s
 - Uses very low-loss material
- Cable assembly must be updated for 32GT/s signaling transmission
 - Working with two vendors to source
 - Lab experimentation required

OCP NIC 3.0 TTF in use

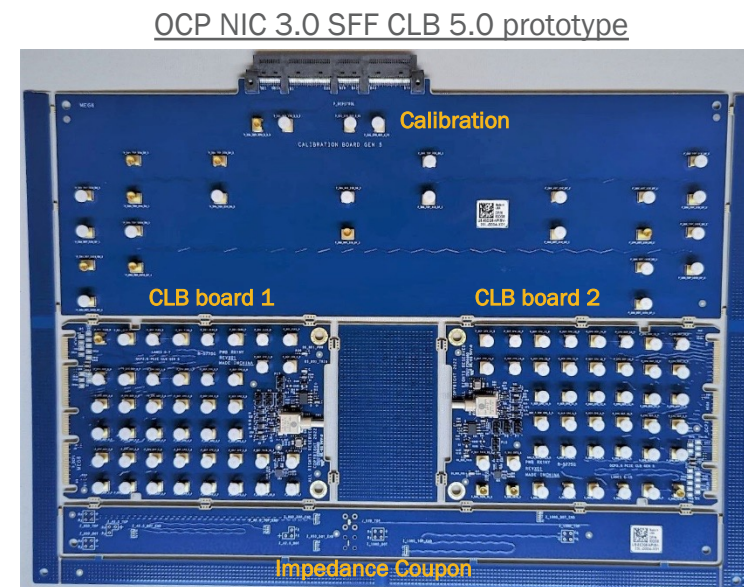


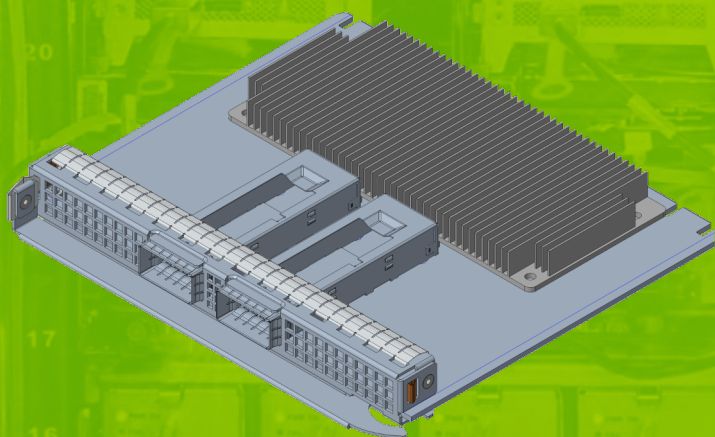
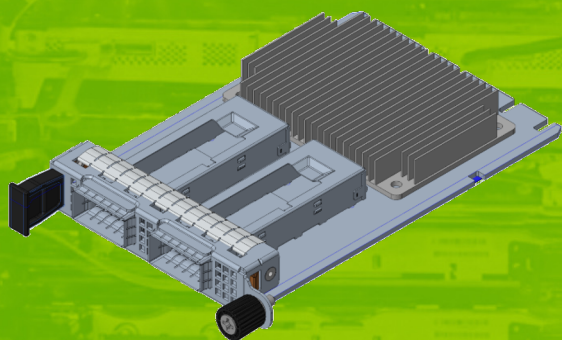
Example cable assembly from Samtec



PCIe® 5.0 SI Test Fixtures

- CLB 5.0 32GT/s status:
 - Based on 4.0 design
 - Gerbered in January; PCB arrived March
 - Assembled protos readied March 30
 - Testing underway
- CBB 5.0 32GT/s status:
 - Based on 4.0 design
 - Updates to align with PCI-SIG®
 - Routing starts once CLB verified



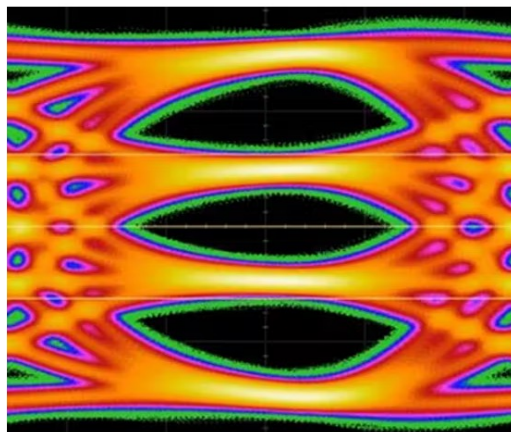


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Damien

Next-Gen Speed Bump

Trending Speed Upgrade with backward compatibility



RMII

SFF
LFF
TSFF

Support PCIe Gen6
thru SFF-TA-1002

>100Mbps OOB still
under investigation

Form factor trending
NO CHANGE

Insertion loss &
crosstalk of 64G
PAM4 yet to be
in spec

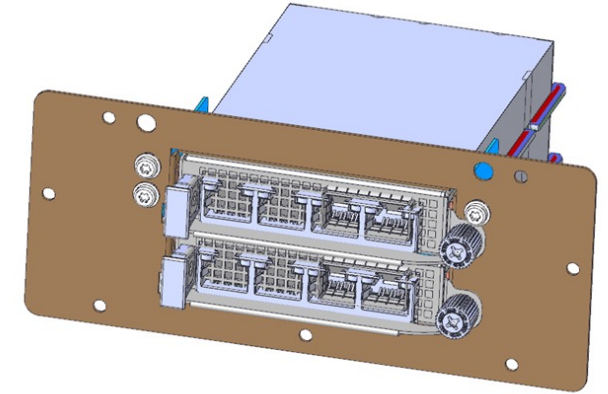
Backward
compatibility is
the priority



Specification & Test Fixture for Tall Small Form Factor

17.8mm

Side View (TSFF)



Released Specification Docs

Name	Format	Version	Date	Contributor	Link
OCP NIC 3.0 Specification (with change bar)	PDF	1v20 Released	09/13/2021	OCP NIC sub-group	Download
OCP NIC 3.0 Specification (without change bar)	PDF	1v20 Released	09/13/2021	OCP NIC sub-group	Download

Fixtures Design Files

Name	Format	Version	Date	Contributor	Link
OCP NIC 3.0 PCIe® Gen3 CLB/CBB Fixtures Collateral (Schematics, Layout & BOM)	ZIP	N/A	6/13/2019	Dell-EMC	Download
OCP NIC 3.0 PCIe® Gen4 CLB/CBB Fixtures Collateral (Schematics, Layout & BOM)	ZIP	N/A	6/13/2019	Dell-EMC	Download
OCP NIC 3.0 Shock & Vibe Fixtures Collateral (Mechanical drawings)	ZIP	N/A	3/29/2019	Intel	Download
OCP NIC 3.0 SFF Thermal Test Fixtures Collateral (Mechanical)	ZIP	N/A	6/4/2018	Dell-EMC	Download
OCP NIC 3.0 TSFF Thermal Test Fixtures Collateral (Mechanical)	STP/ZIP	N/A	10/20/2021	Dell-EMC	Download
OCP NIC 3.0 Thermal Test Fixtures Collateral (Schematic, Layout)	ZIP	N/A	2/11/2019	Dell-EMC	Download

Feedback for current & next-gen OCP NIC

We would love to hear your thoughts or feedback on how we can improve your experience!

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Feedback Type *


☐ Bug Report

☐ Comment

☐ Improvement suggestion

☐ New feature request

☐ Other...

Feedback *

Long answer text

Name (to be contacted for discussion if needed) *

Short answer text

Feedback Form

tinyurl.com/feedback2ocpnic

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Call to action

Join discussion

opencompute.org/projects/project-and-ic-meetings-calendar

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Questions & Answers



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Jason

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