

OAM Reference System Overview

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OAM Reference Systems **Design Joint Review**



PLATINUM



OAM Reference System Overview

Inspur 21 inch Co-Planar system



- 21 inch 3OU, 34.6" (800mm) depth
- 8*OAMs
- UBB: Combined FC+ 6 port HCM Topology
- 4*PCIE Gen4 x16 Link to connect Hosts
- 4*PCIE Gen4 x16 Slots support 100G Infiniband or Ethernet for expansion





- 19 inch 6RU, 30 inch (762mm) depth
- 8*OAMs
- UBB: Combined FC+ 6 port HCM Topology lacksquare4*PCIE Gen3x16 slots for host uplink lacksquare12*PCIE Gen3 x16 slots for flexible IO

expansion



Hyve Design Solutions 19 inch Stack System

(PCIE interface will be revised to Gen4 in next release.)

ZT Systems 19 inch Co-Planar System



- 19 inch 4RU, 34.6" (880mm) depth
- 8*OAMs
- UBB: 8-port HCM topology
- 2*PCIE Gen4 x16 Uplinks for Multi-Host
- 4*PCIE Gen4 x16 Slots
- 4*2.5" NVME hot plug drives in front



Inspur OAI Reference System

Adam Ting System Engineer Inspur





OAM Reference Systems **Design Joint Review**





PLATINUM



Content

- System Feature List
- System Architecture
- Board List
- Power Distribution
- Host Connectivity
- Thermals simulation
- Power simulation
- Sample schedule





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System spec

	Design spec 21"
Chassis	3OU W/H/D =537mm x 141mm x 800mm
PCIE	PCIE Gen4
OAM	8 x OAM TDP:300W – 450W
Power	54V to 12V PDB
HIB	Support PCIE Gen4 switch 4 x PM42100 single layer.
FAN	4 x 9256 FAN
Extension	4 x PCIEx16 Slot; 2 x PCIE x 32 whisper; 8 x
BMC	AST2520 with RJ45 management port

























Rear View

















I/O Module Operating



The I/O module be lock by toolless latch.

After release latch, the labor-saving lever can help to release module.







The lever can help to pull whole I/O module out.







UBB Dimension and connector







PDB - 54V to 12V









Host Interface Board



Front IO



- ✓ Size change to Width 70mm , Length 58.5mm.
- ✓ Add USB3.0 port.









DC topology – 54V HSC in PDB,12V E-fuse in UBB







54V to 12V: DCM3717 54V HSC: ADM1272 12V E-fuse: MP5023







System Topology : Single Node connect 8*OAMs









48V V2 Open Rack OAM Solution

48VDC V2 Open Rack

- 1 pairs 48V Bus Bar
- 1 shelf per Rack
- 15 PCS 2.7KW BBU and back up time 2 Minutes

Power Shelf

- 33KW(12xPSU) \bullet
- 40V-58V \bullet
- 93mm x 534mm x 594mm \bullet







OAM Vendor

- Intel Nervana NNP-T \bullet
- Habana Gaudi \bullet
- Nvidia ullet
- AMD ullet

System Devices

- 4x Inspur 3OU OAM systems ullet
- 8x Nodes ullet

System	Power	Subtotal
Inspur 30U OAM System	20KW (4x ~5000W)	25 6K/M
Head node	5.6KW (8x700W)	23.0KVV
Total Power/Rack		33KW









System Configuration & Thermal Design Power



UMMIT



	Thermal Design Power		r	
	Component	Unit Power	Qʻty	Pov
	ΟΑΜ	450	8	360
Re-timer x16	Re-timer	5	16	80
	Switch	60	4	24
	PHY	12	8	9(
	QSFP-DD	12.5	8	10
	CX5	18.1	4	72
	VR	25	6	15
	9256 Fan	140	4	56
	Tota	Power (watt)		489
	*TDP in this tab	le will modify when	data update	





Simulation Results (Temperature Field)

Simulation Boundary Condition Setup

- TDP at 35C ambient >
- Fan normal >
- Sea-level
- Compact model for upper stream NNP HS
- Compact model HS for VR
- Symmetric placement for switch HS

two for detail model; two for compact model



Temperature Results			
Location		Spec (C)	Temp
NNP	ASIC	115.0	76.5
	HBM	95.0	93-3
Re-timer		85.0	82.8
Switch		115.0	90.8
PHY		110.0	72.7
QSFP-DD		70.0	66.9
CX5		105.0	94.1
VR		125.0	108.7



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Current Density Plot L11

Number	Power rail
1	P12V_VR0
2	P12V_VR2
3	P54V_0
4	P54V_1
5	P54V_3
6	P54V_2
7	P54V_5
8	P54V_4
9	P54V_6
0	P54V_7





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The higher current density is around 35A/mm2



Current Density Plot L12

Number	Power rail	600 /00
1	P12V_VR0	
2	P12V_VR1	
3	P12V_VR3	
4	P12V_VR2	
5	P12V_0	6k)
6	P12V_1	
7	P12V_2	
8	P12V_5	
9	P12V_6	
A	P12V_4	4 3
В	P12V_7	The higher current of







density is around 35A/mm2



Head node connection – whisper to whisper









Inspur Head node with Whisper GEN4 retimer card







Gen4 Re-timer card

4x2.5"HDD **Expansion Slot:** 1x HHFL(x32) : CPU0



Head node support 48V Rack









54V to 12V: DCM3717



Call to Action

- Power on Sample availability: Q4 2019
- Visit Inspur demo System at OAI Experience Lab and Inspur Booth
- Provide feed back to OAI group
- Project Wiki with latest specification : https://www.opencompute.org/wiki/Server/OAI
- Mailing list: https://ocp-all.groups.io/g/OCP-OAI





Hyve Design Solutions **OAI Reference System**

Emilie Yang Technical Program Manager Hyve Design Solutions





OAM Reference Systems **Design Joint Review**





Outline

- System Feature List
- System Architecture
- Board List
- Power Distribution
- Host Connectivity
- Thermals





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Workshops **Summits**



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Feature List

Item	Features
Form Factor	 19" rack mountable. Total 6RU. 30" depth sys with famous fa
Dimension	L 762mm(30") x W 447
Board List	 Universal BaseBoar HIB: PCIE Switch BaseBoar Midplane PDB_Top, PDB_btm
UBB OAM Interconnection Topology	Combined FC+ 6 port H
Host Connectivity	Up to 4 PCIE x16 slots
UBB Scale Out	Up to 8x QSFP-DD (28
PCIE Expansions	Up to 12 PCIE x16 slot
Debug Ports	 1x USB3.0 form fact error message displate 2x MicroUSB OAM I
BMC	AST2520, supports IPN
Management IO	1GbE RJ45 dedicated
Cooling	Eight 8080 dual rotor fa
Power Delivery	54V input from rack bus
Miscellaneous	UID. Power button

an outside of support shelf mm(17.6") x H 261mm(10.27")

d (UBB) oard (PSB)

HCM Topology

on the HIB

BGbps NRZ or 56Gbps PAM4 Serdes interface)

s on HIB

tor debug port to support OCP debug card for BMC ay.

UART ports on UBB

MI2.0

BMC LAN port

ans, support up to 8x 450W OAM, air-cooling @35C

s bar through two power clips



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Workshops Summits

System ISO View







Outline Dimension

- Sys depth 30" with sys fan outside of \bullet support shelf
- 6RU in Height ullet
- 19" rack form factor \bullet

2RU HIB













Front View









Rear View







Fan 54V Busbar power 8080x8 clipx2 Ô (\bullet) 0 \bigcirc \odot 0



System Explode View (1/2) Fans Rear cover **UBB** Tray (removable) **HIB** Tray Top cover Shelf (none-removal) Inner rail PCIe card retention cover







UBB Board Placement





90.4mm(3.56") 25mm(0.984") JX410-51352 power conn 12V retime 10028917-001LF OAM 0 2x2 power conn 54V OAM3 et l 6) 10131762-101LF ExaMAX 6p8c etimer OAM4 10028917-001LF 2x2 power conn 54V OAM7 JX410-51352 power conn 12V 585mm(23.03")



HIB Board



Middle Plane Board



UMMIT









PDB_top Board

268mm(10.55")



- PDB_top: 54V input from rack bus bar
- 54V delivered to PDB_btm through PDB bus bars
- supplies 12V to HIB
- Supplies 54V to fans





PDB_btm Board

• PDB_btm supplies 54V and 12V to UBB.

406mm(16")





G88040153T4HR HIB/UBB Signal ۲ Û 0 \odot ۲ 107mm(4.21") ٩ 10106267-0C00403LF Power conn, 12V For UBB 449142401 Power conn, 54V For UBB

















Power Delivery





Bottom-PDB



Power cabling



Host Connectivity



MiniSAS HD to MiniSAS HD Cable

PCIE Topology 2 : 2 x16 PCIE upstream from host











Thermal Modeling



Thermal Power Budget and Simulation Result

Component	TDP, W	Qty	Total TDP, W	Thermal Spec, C	Worst Result, C
OAM ASIC	450	8	3600	Tj=115	104
OAM HBM				Tj=95	93
Gen3 Switch	58.3	4	233.2	Tj=110	98
PHY Re-timer	7	8	56	Tj=105	68
Retimer	7	16	112	Tj=105	99
PCIe card(LP)	25	12	300	55C/200LFM	35C/300LFM
Total	TDP, W		4301.2		







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- Mailing list: https://ocp-all.groups.io/g/OCP-OAI







ZT Systems OAI Reference Design

Mark D. Chubb, Director of Architecture, ZT Systems (In partnership with Inventec)





OAM Reference Systems Joint Review





Agenda

- System Features
- Mechanical Overview
- System Architecture
- Board List
- Power Distribution
- > Thermals







System Features

	OAI
Chassis Dimensions	435mm width x 880mm length x 17
Rack Infrastructure	19" Rack
Host Node Connectivity	Non-Integrated External Host
Universal Baseboard	Supports 8x OAM modules in a 8-p
Host Interface Board	Co-planar connectivity to UBB, hos
Expansion Slots	Support 4* PCIe Gen4 x16 Single
Storage	Front: 4* 2.5" NVME hot plug drive
Power	3000W Platinum PSU 54V for 2+2
System Management	Aspeed 2520, IPMI v2.0 Compliant
System Fans	5x 8080(80mmx80mmx80mm) dua
IO Rear	PWR button, RST button, UID button MGMT (RJ45), Com port, USB 3.0
IO Front (Optional)	PWR button, RST button, USB 3.0





75mm height (4U)
port Hybrid Cube Mesh (HCM) topology
osts BMC, 3 Broadcom Gen4 Switches, and PCIe expansion slots
e width including 2 * PCIe Gen4 x16 Uplinks for multi-host
e (Option)
2
nt
al rotor fans
ton, System Health LED, 8x OAM Health LED 0 (USB 2.0/UART/I2C)
0 (USB/UART/I2C)



System 3D View







435 mm



Top and Side View

Front









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Rear



Front View







Rear View





PCIe Gen4 X16 4 x rear NVME ۲ ΘΞ ۲ MSHD#1 for uplink USB 4x 3000W PSU for uplink MGMT C20 AC Inlet PWR/UID button



UBB Tray Serviceability

UBB tray have the cam mechanism on handle for HD connector mating/un-mating

Note: The working group is studying common tray compatibility across the different reference systems







System: Exploded View









UBB 8-port HCM topology







UBB 8 Port HCM topology

• HCM using 8 ports: 1L, 1H, 2, 3, 4, 5, 6, 7	Connec
SerDes Port 2, 3, 4, 5, 6, 7 are x8 lanes	
 SerDes Port 1 is x16 (2 x8) lanes 1L – SerDes 1 Lower 8-bit 1H – SerDes 2 Upper 8-bit 	Connec
 Ports 4 and 6 (OAM #1, #2, #5 and #6) Ports 4 and 6 on the bottom row are cabled connections to support full 8 port HCM. 	Connect
Alternatively, Ports 4 and 6 can be used for OAI expansion (scale out) via QSFP-DD cables to another UBB board.	Conne











Board List

PCB#	Description	Dimension WxL (mm)	PCB Layer	PCB Materials	PCB Thickness (mm)
1	Universal Baseboard (UBB)	417.00 x 585.00mm	22L	Hyper Low Loss	3.26mm
2	Host Interface Board (HIB)	415.30 x 259.60 mm	16L	Ultra Low Loss	2.49mm
3	Fan board	407.42 x 52.83 mm	6L	Normal	1.60mm
4	Power Distribution Board (PDB)	314.96 x 75.44 mm	10L	Normal	1.60mm







Universal Baseboard



OAM





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585mm

417mm



HIB Switch Board





259.60mm SW2 =0 Fan board signal llo HS for 54V/12V VR Module ► 4 x PCIe X16 Gen4 slot ► PDB signal SW1 Power conn from PDB (for UBB (†) 54V/12V-2x8 connector) ۲ Signal to front IO board 415.30mm

*P12V VR: Vicor 3717(54V to 12V VR solution)



Fan Board









PDB Placement







314.96 mm



Power Delivery







PDB SW board 29A 12V VR **OCP:18A** 12V VR 15A 54V HSC 12A 54V HSC **3KW PSU** 12V VR 15A 54V HSC 2+2 12A 54V HSC 54V PSU 33.5A 12V VR 54V HSC 54V PSU E OCP:40A -PCIE x4 -PCIE switch x3 80x80 FAN x5 -VRs 54V PSU 2.2A STBY VR -BMC 54V PSU 12A -VRs 54V HSC 15A 54V HSC 12V VR 12A 54V HSC 15A 54V HSC 12V VR



Thermal Design Thermal System Model

- 8* OAM module(up to 410W)
- 5* 8080 Fan
- 1* Switch board
- 4* PCIe card









CFD Results

System passes

- System Airflow 477 CFM
- HBM memory is close to spec

Next Steps

- Simulate up to 450W TDP .
- Fine tune the air duct to improve margins by increasing air flow to the down stream OAMs
- Simulate fan fail conditions







Spec.	Components	Temperature('C	
	Condition	900m Altitude	
105	OAM ASIC Die	99.5	
95	OAM HBM	94.5	
70.0	Front QSFP-DD Tc	65.6	
110	Switch 88096	89.1	
105	PCIe NIC	94.1	



Call to Action

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- Provide feed back to OAI group
- Project Wiki with latest specification : <u>https://www.opencompute.org/wiki/Server/OAI</u>
- Mailing list: https://ocp-all.groups.io/g/OCP-OAl





Speakers' Profile



Adam Tin

System Engineer at Inspur based in Taiwan. Over 16 years of server hardware design and architecture. As system leader to provide various form factor of server solution for target customer in US and Europe.



Emilie Yang Emilie is a senior Technical Program Manager at Hyve Design Solutions in Taiwan. She contributed several OCP hardware and cooling designs in past decade. Now, she continuously delivers various products through program management and technical expertise to OCP community and customers.





Mark D. Chubb

Mark is the Director of Architecture at ZT Systems. Mark is based in Seattle, WA and works closely with key Cloud Customers to conceptualize and develop Server and Rack solutions for Hyperscale computing needs. Mark has over 20 years of experience in HW Design and Architecture of Server class boards and systems. Mark is an active participant in various OCP Forums and has provided talks at prior OCP Summits about ZT Systems design contributions to OCP.









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OCP Regional Summit 26–27, September, 2019





ZT Systems BACK UP SLIDES






Cable routing – PDB power and signal cable to Switch board









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2X8 power cable (660mm, 16AWG) 2X5 power cable (670mm, 16AWG)
PWR Signal cable (630mm, 24AWG)





Cable routing – fan board signal to Switch board ; PDB power to switch board







Fan board signal cable (70mm, 24AWG)



Cable routing – fan board signal to Switch board ; PDB power to switch board











Power cable (865mm, 16AWG)



Fan signal cable (70mm, 24AWG)



Internal Cable List

ltem	Description	Quantity	AWG	Length	Note
1	PDB to Switch board power cable	2	16	660mm	
	(for UBB 48V/12V-2x8 connector)		10	ooonini	
2	PDB to Switch board power cable	1	16	670mm	
	(for Switch board and UBB 12V_M-2x5 connector)	•	10	07011111	
3	PDB to Switch board signal cable	1	24	630mm	
4	Fan board power to PDB power cable	1	16	865mm	
5	Fan board to Switch board signal cable	1	24	70mm	
6	Silmlinex8 cable from switch board to NVMe BP	2			Option
7	NVMe BP power from PDB	1			Option
8	NVMe BP signal from Switch board	1			Option







Cable routing – PDB power and signal cable to Switch board







- 2X8 power cable (660mm, 16AWG) 2X5 power cable (670mm, 16AWG) PWR Signal cable (630mm, 24AWG)





PCBA Design

UBB Stackup

- ➢ PCB layer: 22L
- ► PCB material: Hyper low Loss
- ➢ PCB thickness: 128 mil (3.25mm)

For 22-Layer Stack-up H=3.7/4(N	~		Breakout (I	mils) 🗸 🧹	
(if there is a "Space" existed in t columns, the cell will be highligh please remove it)	Impedance (Ω)	Width (W)	Spacing (S)	Maximal length	
PCIE	Diff	85	4	4	TBD
PCIE	Diff	85	4	4	TBD
ICL	Diff	90	4	4	TBD
CLK	Diff	85	4	4	TBD





	Dielectric and Copper Prop				
Layer			Copper		
Name	Description	Type	Wright	Type	
Top SM	Top Soldermask	Soldermark	an cargane		
I 1top	Signal Liton	Signal	0.5 oz. platod	UTE	
PP I Iton I 2	Prenucz	PP			
11_D100_DD	GND L2	GND	1.0 oz.	HVLP	
Core 1.2 1.3	Corc	Com	110 01		
L3	Signal L3	Signal	1.0 oz.	HVLP	
PP L3 L4	Prepreg	PP			
L4	GND_L4	GND	1.0 oz.	HVLP	
Core L4 L5	Core	Corc			
L5	Signal_L5	Signal	1.0 oz.	HVLP	
PP_L5_L6	Proprog	PP			
L6	GND_L6	GND	1.0 oz.	HVLP	
Core_L6_L7	Core	Core			
L7	Signal_L7	Signal	1.0 oz.	HVLP	
PP_L7_L8	Proprog	PP			
L8	GND_L8	GND	1.0 oz.	HVLP	
Corc_L8_L9	Corc	Core			
L9	Signal_L9	Signal	1.0 oz.	HVLP	
PP_L9_L10	Proprog	PP			
L10	GND_L10	GND	1.0 oz.	HVLP	
Corc_L9_L10	Core	Core			
L11	PWR_L11	PWR	2.0 oz.		
PP_L9_L10	Prepreg	PP			
L12	PWR_L12	PWR	2.0 oz.		
Corc_L12_L13	Core	Core			
L13	GND_L13	GND	1.0 oz.	HVLP	
PP_L13_L14	Proprog	PP			
L14	Signal_L14	Signal	1.0 oz.	HVLP	
Corc_L14_L15	Corc	Corc			
L15	GND_L15	GND	1.0 oz.	HVLP	
PP_L15_L16	Proprog	PP			
L16	Signal_L16	Signal	1.0 oz.	HVLP	
Corc_L16_L17	Core	Core			
L17	GND_L17	GND	1.0 oz.	HVLP	
PP_L17_L18	Frepreg	PP			
L18	Signal_L18	Signal	1.0 oz.	HVLP	
Corc_L18_L19		Core		11177	
L19	GND_LIV	GND	1.0 ez.	HVLP	
PP_L19_L20	Proprog	PP	1.0	TTTTT	
L20	Signal_L20	Signal	1.0 ez.	HVLP	
Corc_L20_L21		Core	1.0	TTTTT	
L21	D	GND	1.0 oz.	HVLP	
PP_L21_L22bot	Frepreg	PP	0.5	TTTTT	
L22bot	D_6 C_14 1	Signal	U.S oz. plated	HIE	
Bot SM	Bot Soldermask	Soldennask			



DCRA Docian						Layer	Nominal Board thickness 97.6 MIL Solder Mask	Thickn ess (MIL)	Type Solder mas	
	1 レビン						L1	0.5oz+platting	2	Signal
		.9						PP	2.7	
							L2	1oz	1.2	GND
HIB Switch	Board Stac	kup						Core(1/1)	4	
							L3	1oz	1.2	Signal
								PP	4.5	
➢ PCB layer: 10	6L						L4	1oz	1.2	GND
								Core(1/1)	4	
PCB materia	I: Ultra Low Lo	SS					L5	1oz	1.2	Signal
								PP	4.5	
PCB thicknes	ss: 97.6 mii (2.4	48mm)					L6	1oz	1.2	GND
								Core(1/1)	3	
For 16-Laver Stack-up H=3 7/40	MS/SL)						L7	1oz	1.2	Signal
(if there is a "Snace" existed in t	the below two	Impedance (0)		Breakout (r	niis) 👻	Remark		PP	12	
columns the coll will be highlig	bted in dark red and	impedance (sz)	Width	Spacing	Maximal	INCHIGUR 1	L8	2oz	2.4	PWR
plana ramava it)	ineu in uark reu anu		(w)	(S)	length			Core(1/1)	4	
PCIE	Diff	 	4	4	TRD	TRD	L9	2oz	2.4	PWR
PCIE	Diff	85	4	4	TBD	TBD				
	Diff	85	4	-	TBD	TBD		PP	12	
120	SE	50	4	4	TBD	TBD	140	4	4.2	Circul
MISC	SE	50	4	4	TBD	TBD		Corol1/1	3	Signal
CLK	SE	50	4	4	TBD	TBD	144	lor	1.7	GND
Power enable	SE	50	4	4	TBD	TBD		PP	4.5	GIND
JTAG	SE	50	4	4	TBD	TBD	1.12	107	1.2	Signal
SPI	SE	50	4	4	TBD	TBD		Core(1/1)	4	orginal
UART	SE	50	4	4	TBD	TBD	113	107	12	GND
USB	ush	85	4	4	TBD	TBD		PP	4.5	
				•			L14	1oz	1.2	Signal
BCM54612	diff_MDI	100	4	4	TBD	TBD		Core(1/1)	4	
BCM54612	Single-End	50	TRD	TRD	TRD	GTXCLK	L15	1oz	1.2	GND
DOMOTOTZ	Single End	00	100	100	100	OTXOLK,		PP	2.7	
							L16	0.5oz+platting	2	Signal/PWF
								Solder Mask	UO	Joorder mas



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PCBA Design

HIB Switch Board CLK Block Diagram











Fan Board Block Diagram

Block diagram







PDB Block Diagram









Thermal Design

Air Duct









Thermal Design (BU)

System Fan

- 8080 Fan (48V~60V)

- Fan speed: F17000/R13000rpm
- Max power:~177 Watts









OAM on system1 to OAM on system2 Topology

Length evaluation •









OAM on system1 to OAM on system2 Topology

Loss table

Total loss @ 14GHz	Hyper Ultra Low loss (dB)	Ultra Low loss (dB)	Low loss
OAM on System1	8	8	8
Via 1	0.5	0.5	0.5
PCB TL1 estimation (5.5")	4.356	5.269	6.24
Via 2	0.5	0.5	0.5
QFSP-DD Conn. (worst case)	1.5	1.5	1.5
QFSP-DD Cable	0.288	-1.538	-3.49
QFSP-DD Conn. (worst case)	1.5	1.5	1.5
Via 3	0.5	0.5	0.5
PCB TL2 estimation (5.5")	4.356	5.269	6.24
Via 4	0.5	0.5	0.5
OAM on System2	8	8	8





1.5 0.5 248 0.5 8

*We only have **0.288 dB** loss margin for cable even we use hyper ultra low loss material.** We have to use active cable or have re-timers on the UBB.

PS. By JDA decision, will use re-timers on the UBB



OAM to OAM (28G SerDes) Topology

Connector

Connector 0









OAM to OAM (28G SerDes) Topology

Loss table

Hyper Ultra Low loss (dB)	Ultra Low loss (dB)	Low
8	8	
0.5	0.5	
8.16	9.87	
0.5	0.5	
8	8	
25.16	26.87	
30	30	
Low Risk	Low Risk	N
	Hyper Ultra 8 0.5 8.16 0.5 8 25.16 30 Low Risk	Hyper Ultra Low loss (dB)Ultra Low loss (dB)880.50.58.169.870.50.58825.1626.873030Low RiskLow Risk

We only have 13 dB loss margin for routing on PCB.

Acceptable length	Hyper Ultra Low loss (inch)	Ultra Low loss (inch)	Low lo
PCB TL	16.41	13.57	11





oss (inch)

1.44



Switch to UBB (OAM) PCIe Gen4 Topology

The worst case length evaluation







OAM

Switch



Switch to UBB (OAM) PCIe Gen4 Topology

• Loss table

Total loss @ 8GHz	Hyper Ultra Low loss (dB)	Ultra Low loss (dB)
PCle Gen4 Switch (PEX88096)	3	3
PCB TL1 estimation (4.6")	*0.524=2.4104	*0.621=2.8566
ExaMax Conn.	1	1
PCB TL2+TL3 estimation (17")	*0.516=8.772	*0.607=10.319
Cap. loss	0.5	0.5
Total Via loss (<25mils stub)	6*0.27=1.62	6*0.27=1.62
OAM	5	5
Total Topology loss	22.3024	24.2956
PEX88096 Spec.	28	28
Evaluation Result	Low risk	Low risk





