

# Ampere for openEDGE

October 2019

*Consume. Collaborate. Contribute.*



**OPEN**  
Compute Project

# Who is Ampere?



We are a passionate, experienced team committed to building & delivering what comes next for cloud and edge computing.

*Consume. Collaborate. Contribute.*

# Ampere Product Family

Planned

5nm

Defined

7nm

**"Next Gen  
Ampere"**

Developed

7nm

- Arm v8.2+
- Improved IPC
- Higher power efficiency
- Enhanced scalability of cores, memory bandwidth, IO
- Greater TDP range

**Ampere eMAG™**

Shipped

16nm

- Arm v8.0
- Up to 32 Cores
- 3.3 GHz Sustained Turbo
- 8x DDR4-2667
- 42 lanes of PCIe Gen 3
- 4 SATA Gen 3 ports
- TDP: 75-125W

Multi-generation roadmap to a powerful, efficient, and scalable platform architected for the future of the Cloud and Edge

Increase Performance, Power Efficiency, and Scalability of Cloud-to-Edge Platform at Regular Cadence

# eMAG Advantages for Telco and Edge

**High  
performance,  
scalable, and  
efficient cores**

**Same  
architecture as  
Arm devices  
that dominate  
the Edge**

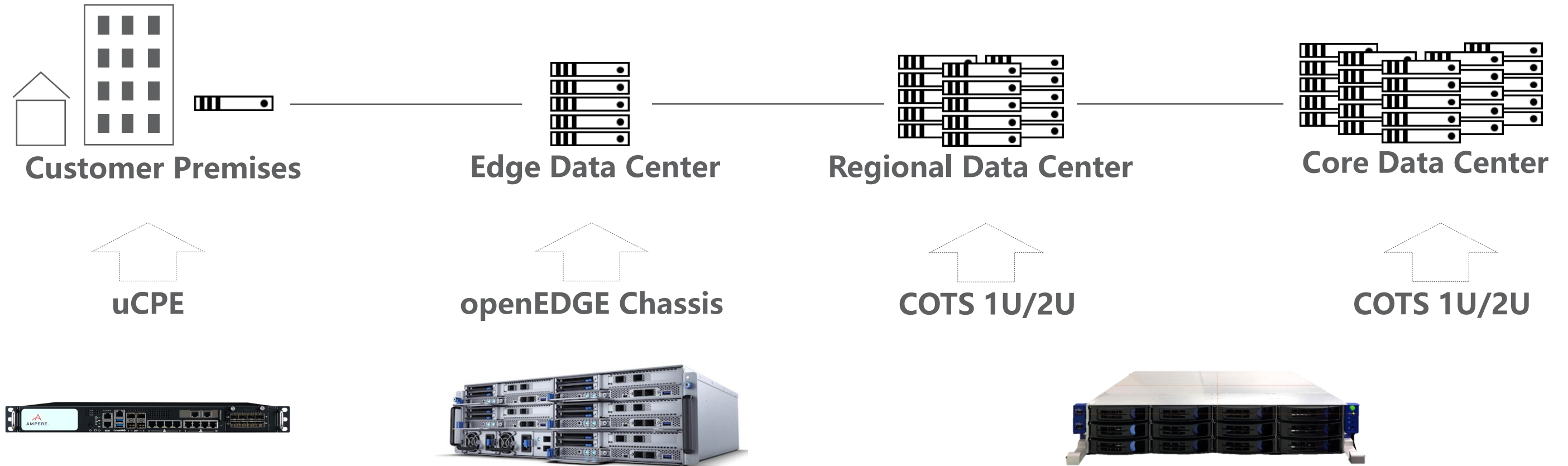
**Security,  
Reliability,  
Scalability at  
the Edge**

**Turnkey  
software and  
hardware  
solutions with  
customizability**





# Ampere eMAG for Wireline Data Networks

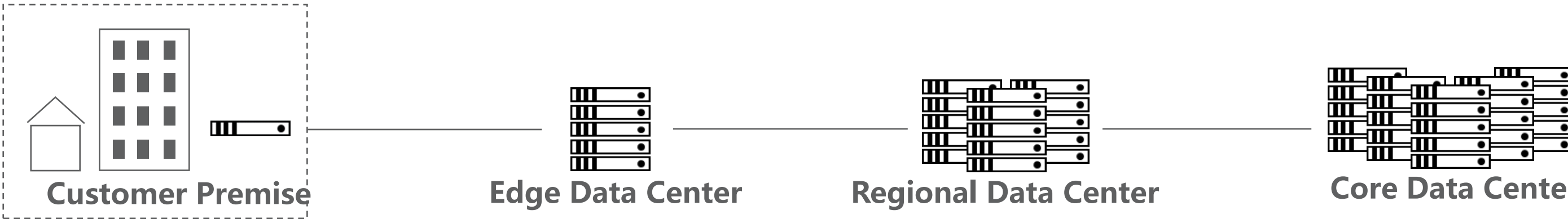


**uCPE : Universal Customer Premises Equipment**  
Based on **eMAG** Arm-based processor  
Runs NVFI, VNFs, customer workloads  
Ampere HW and uCPE Stack available

**SEBA**  
SDN Enabled Broadband Access  
Ported to **eMAG** Arm-based processor  
Source code and demo available

**5G Core**  
"Next Gen Ampere" targeted at Core DC  
High core count, memory channels  
Very high performance compute

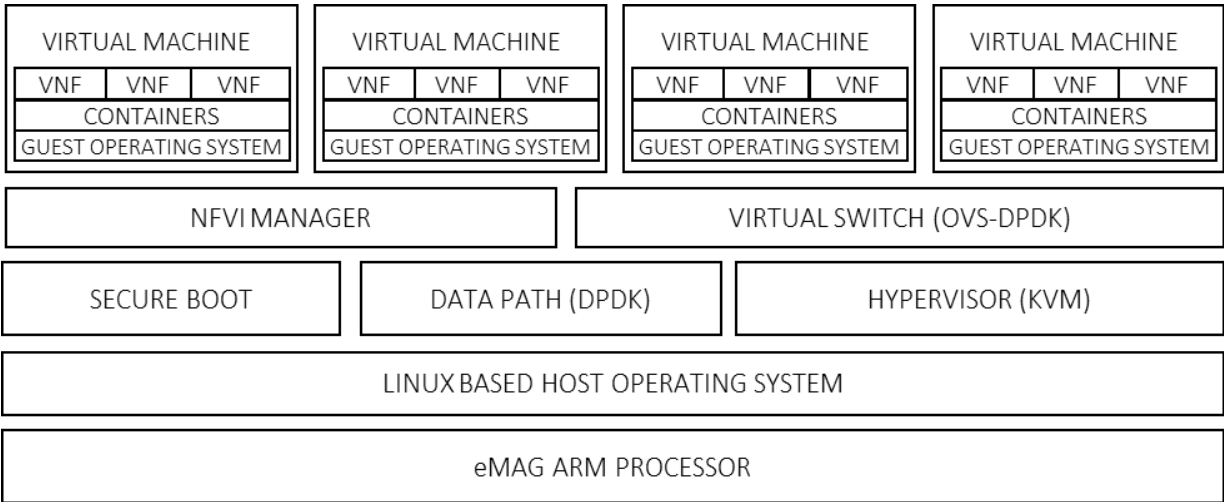
# Ampere eMAG for uCPE



uCPE TYPE			
uCPE Size	SMALL	MEDIUM	LARGE
Approx. System ASP	\$500	\$1000 - \$2000	\$6000 - \$9000
Example x86 CPU	Atom	Xeon D	Xeon SP
x86 Cores	4	8	16-26
Ampere SKU	N/A	eMAG	eMAG & "Next Gen"
Ampere Cores	N/A	16 or 32	32 and Higher
Ampere TDP	N/A	75-125W	125W
# of VNFs	3	4+	10+
Throughput	1 Gbps	1-5 Gbps	> 10 Gbps
RAM	32 GB	64 GB	128 GB
Storage	256 GB	512 GB	1 TB+
LAN 1G Ports	4	8+	8+
WAN SFP 1G / 10G	2 / 0	2 / 2	0 / 4
Power supply	Single/Dual	Single/Dual	Dual
Form Factor	Desktop / 1U	1U	1U



Ampere uCPE 1U hardware Concept

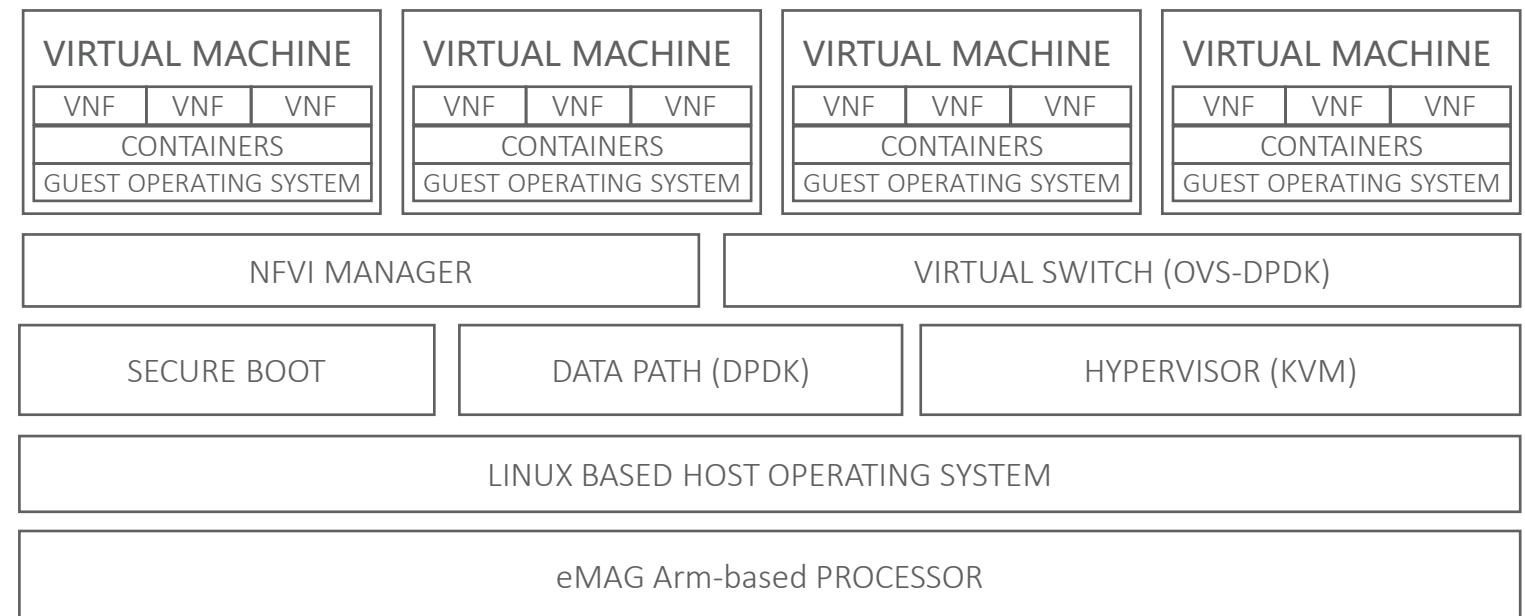


Ampere uCPE Software Stack

# Ampere VNF Ecosystem

- Ampere eMAG processor enables more VNF's in uCPE power and cost envelope
- Ampere is partnering with industry leaders to provide a complete uCPE software
- Ampere plans to contribute Arm-based uCPE spec to OCP

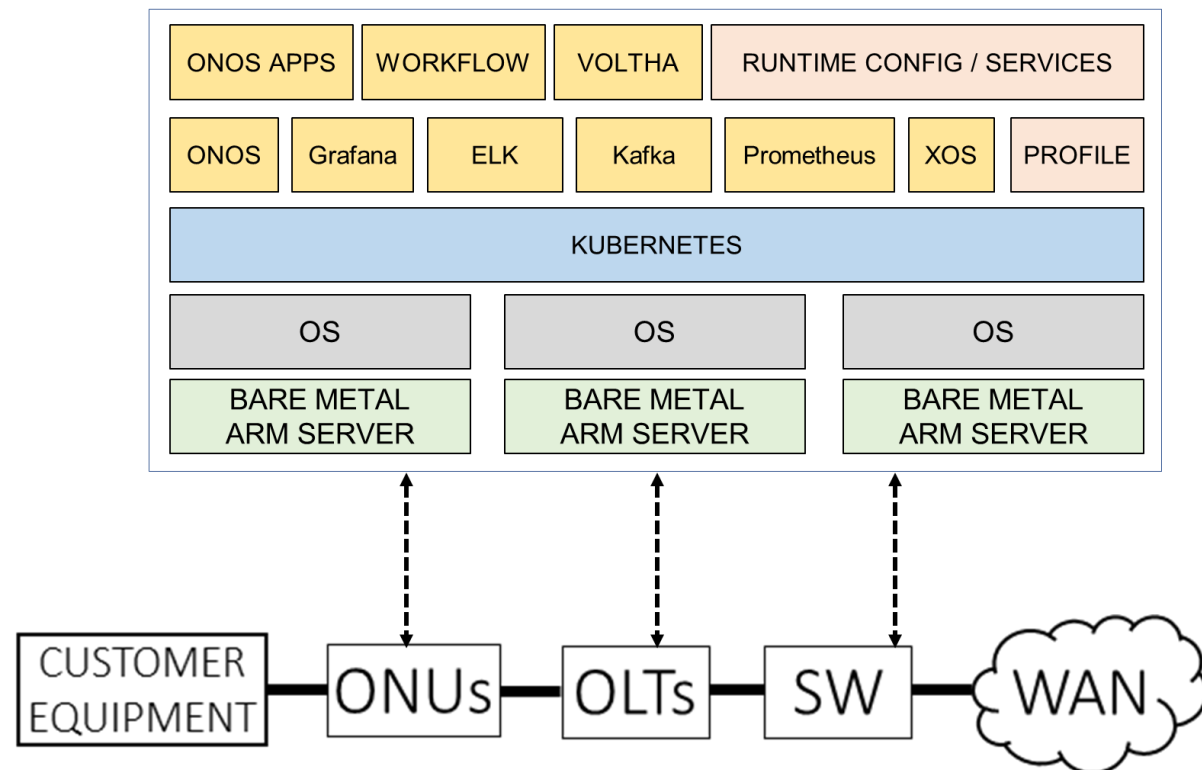
<b>NFVI</b>	Enea, Telco Systems
<b>SD-WAN</b>	FatPipe
<b>Firewall</b>	Checkpoint, Clavister
<b>Routing</b>	6WIND
<b>Security</b>	Trend Micro
<b>Deep Packet</b>	Trend Micro, Enea
<b>Telco</b>	SEBA, RIC/REC



# Ampere eMAG for SEBA



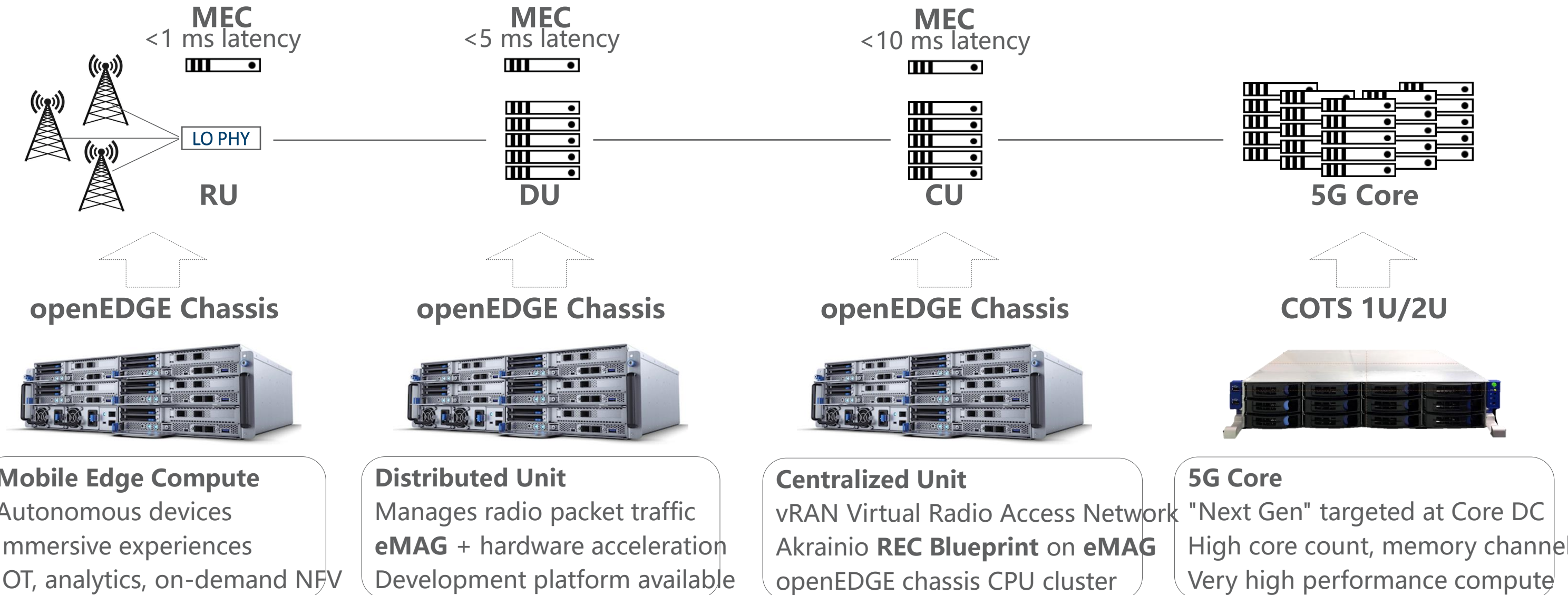
## SEBA



- SEBA: SDN Enabled Broadband Access
- Manages wireline customer broadband services
- Ported to scalable and versatile eMAG Arm-based processor
- Source code and demo available

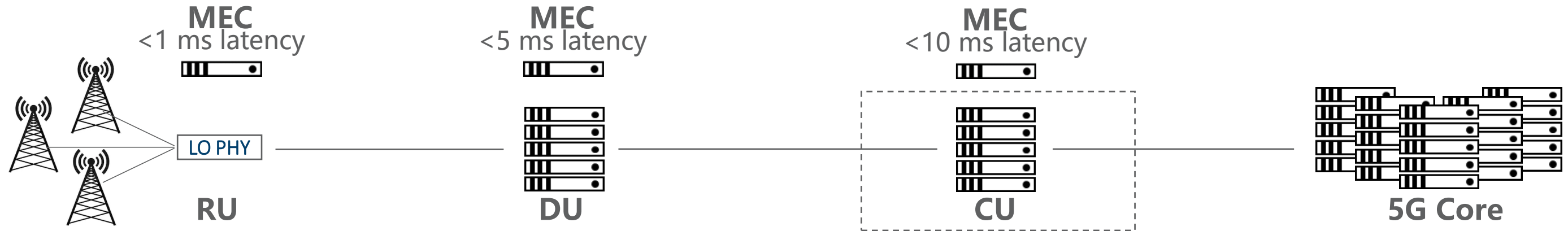


# Ampere eMAG for 5G Wireless Networks

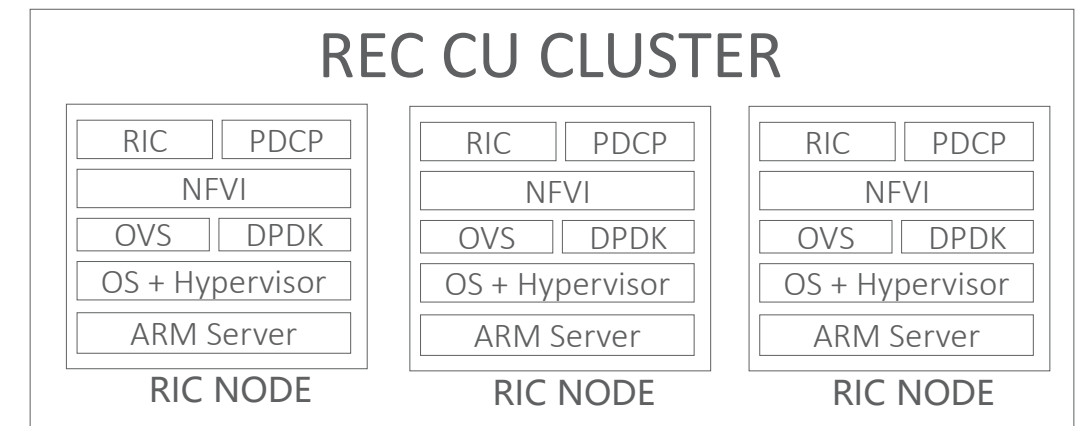


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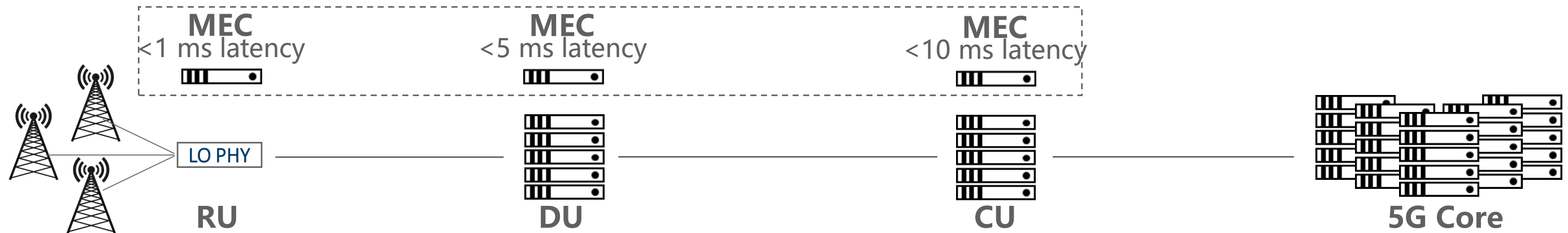
# Ampere eMAG for vRAN



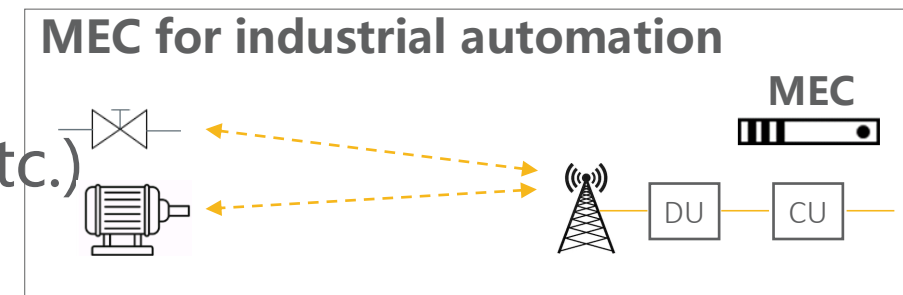
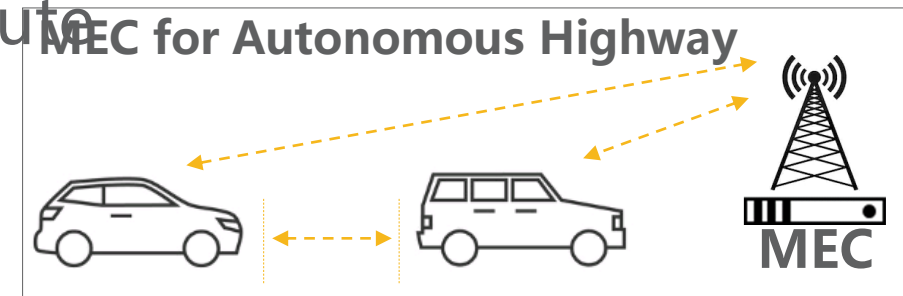
- vRAN: Virtual Radio Access Network
- Implemented with ORAN RIC: RAN Intelligent Controller
- Akraino REC Blueprint running on eMAG processor
- Demo and source code available



# Ampere eMAG for Mobile Edge Compute



- MEC = 5G speed + wireless connectivity + edge compute
- Enables virtual high performance compute in edge devices
- Deploying MEC closer to the edge lowers latency
- Examples:
  - Autonomous infrastructure (driving, traffic control, etc.)
  - Wireless industrial automation
  - IOT gateways, analytics, gaming, content delivery



# Ampere Hawk Motherboard

## Overview

- Compatible with OCP openEDGE Chassis CPU sled
- 6.5" x 13.85"
- 32 and 16 core eMAG SKUs
- 32bit and 64bit Support
- TSMC 16 nm FinFET

## Processor

- 32/16 Ampere eMAG Armv8 64-bit CPU cores @ 3.3 GHz Sustained Turbo
- 32 KB L1 I-cache, 32 KB L1 D-cache per core
- Shared 256 KB L2 cache per 2 cores
- 32MB globally shared L3 cache

## Memory

- 8x 72-bit DDR4-2667 channels
- Up to 16 DIMMs and 1 TB/socket
- ECC, ChipKill, and DDR4 RAS features

## I/O

- OCP Mezzanine v2 (Conn. A/B) 10/40/100 GbE NIC
- 1 x16 PCIe slot
- 2 x M.2 x4 NVME
- 4 x SATA3
- 2 x USB 2.0

## Power

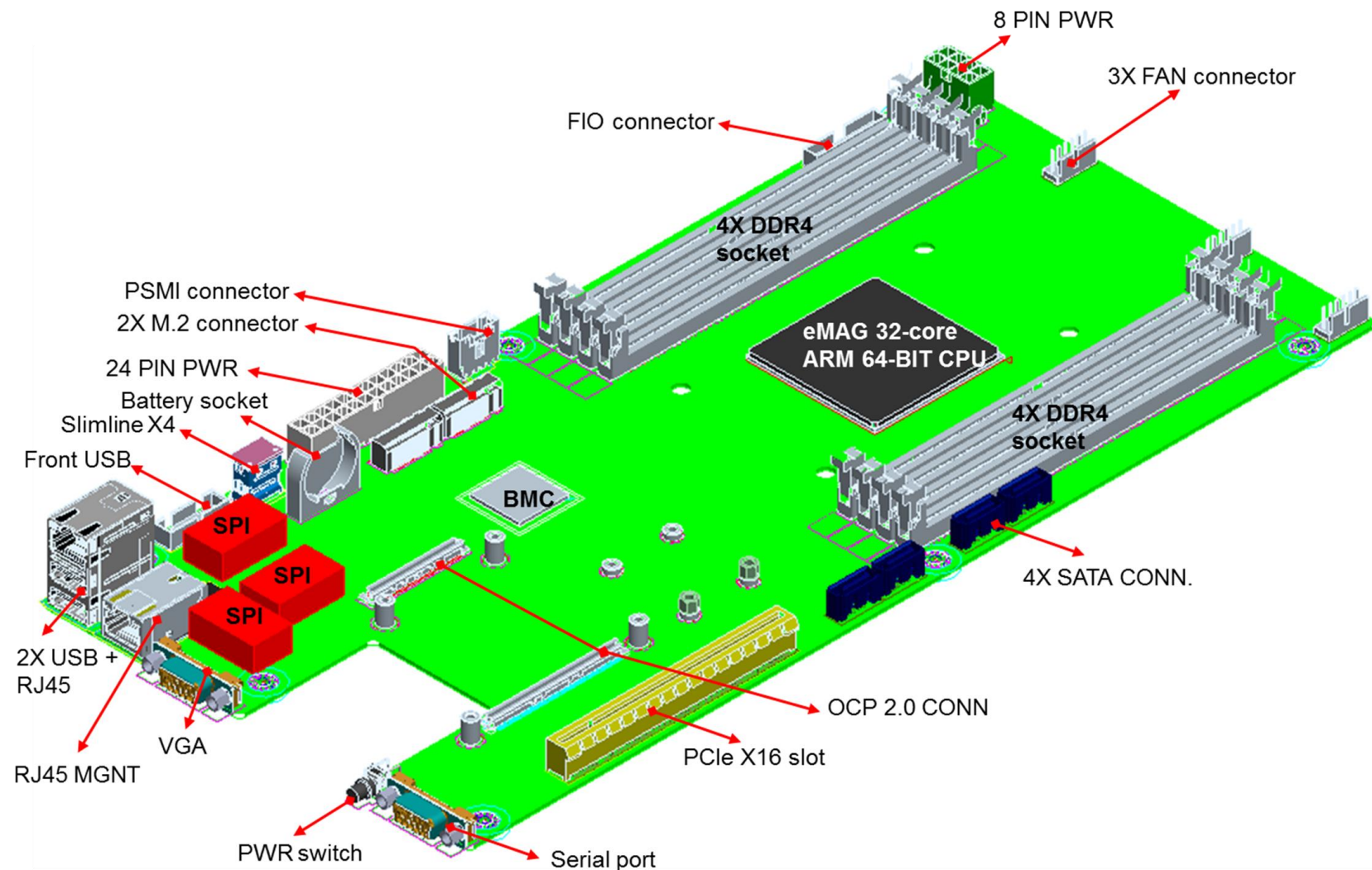
- 125W TDP for 32 cores
- 75W TDP for 16 cores
- Advanced Power Management

## Performance

- SPECrate2017\_int\_peak est.: 68\*
- SPECrate2006\_int\_peak est.: 502\*

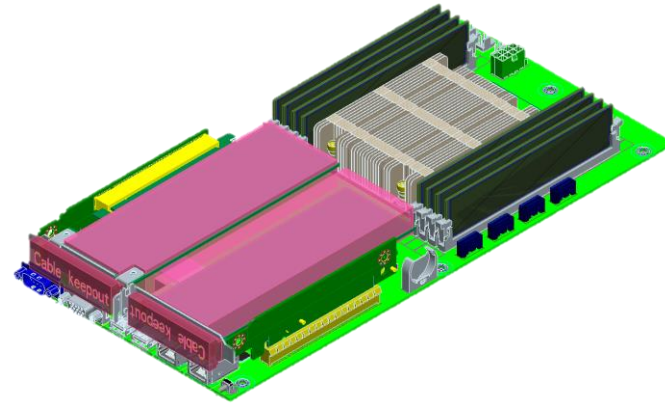
## Availability

- Sampling: Q4'19
- Volume Production: Q1'20



\* Estimated based on measured Ampere 8180 and 8140 using gcc 8.2 -ofast, lto, jemalloc

# Ampere Edge Computing Platforms



OCP  
openEDGE  
CPU Sled



Twin Server

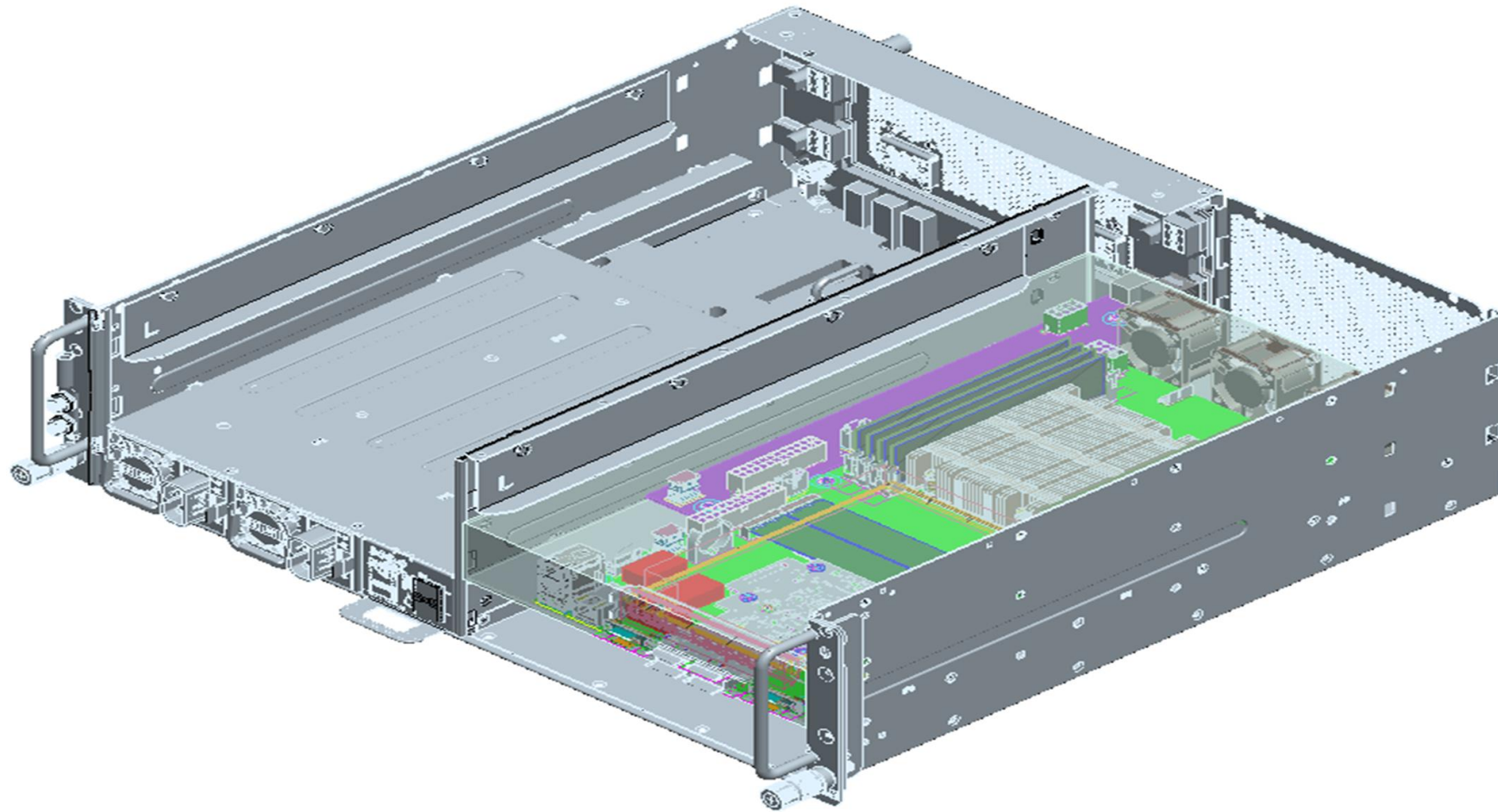


Open19  
CPU Brick





# Hawk in openEDGE / AirFrame CPU Sled



ODM Production by Wiwynn and Available  
Now

*Consume. Collaborate. Contribute.*



# THANK YOU

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